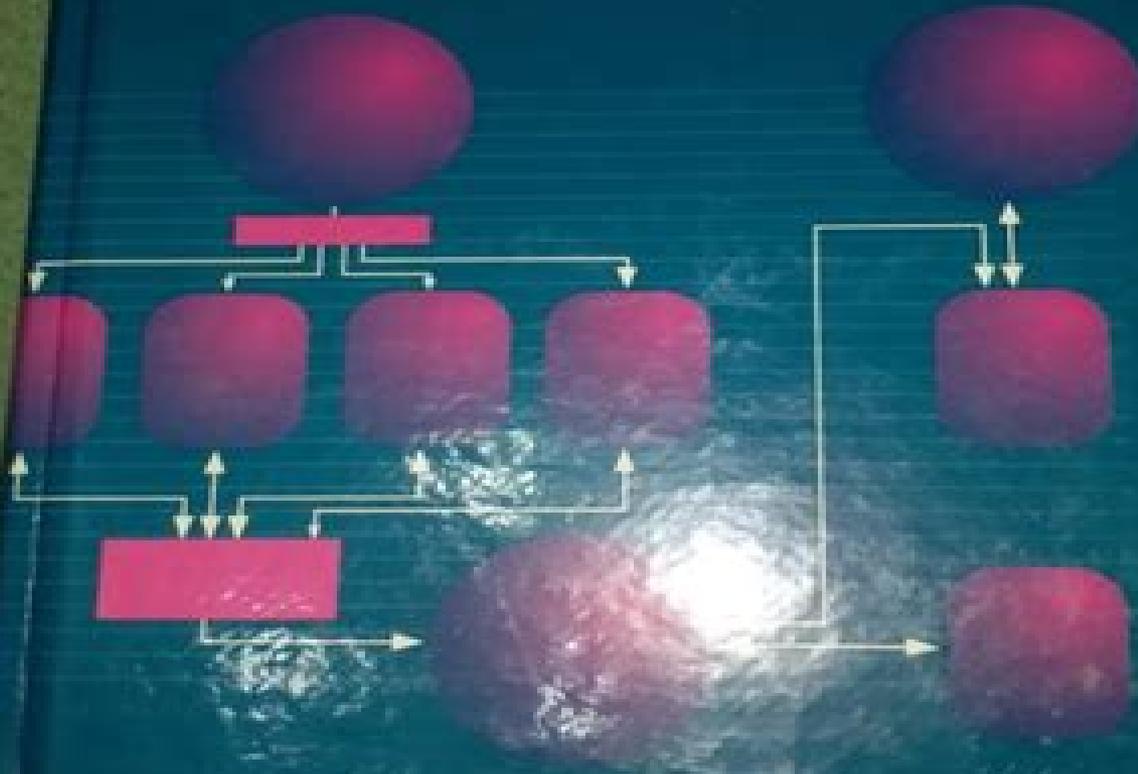


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VLSI

DIGITAL SIGNAL PROCESSORS

An Introduction to Rapid Prototyping
and Design Synthesis



Vlsi Signal Processing Ii

RJ Alexander



Vlsi Signal Processing Ii:

VLSI Signal Processing, II Sun Yuan Kung, Robert E. Owen, J. Greg Nash, 1986 *VLSI Signal Processing Technology* Magdy A. Bayoumi, E. Swartzlander, 2012-12-06 This book is the first in a set of forthcoming books focused on state of the art development in the VLSI Signal Processing area It is a response to the tremendous research activities taking place in that field These activities have been driven by two factors the dramatic increase in demand for high speed signal processing especially in consumer electronics and the evolving microelectronic technologies The available technology has always been one of the main factors in determining algorithms architectures and design strategies to be followed With every new technology signal processing systems go through many changes in concepts design methods and implementation The goal of this book is to introduce the reader to the main features of VLSI Signal Processing and the ongoing developments in this area The focus of this book is on Current developments in Digital Signal Processing DSP processors and architectures several examples and case studies of existing DSP chips are discussed in Chapter 1 Features and requirements of image and video signal processing architectures both applications specific integrated circuits ASICs and programmable image processors are studied in Chapter 2 New market areas for signal processing especially in consumer electronics such as multimedia teleconferencing and movie on demand Impact of arithmetic circuitry on the performance of DSP processors several topics are discussed in Chapter 3 such as number representation arithmetic algorithms and circuits and implementation **VLSI Digital Signal Processing Systems** Keshab K. Parhi, 1999 This graduate level textbook brings together the distinct fields of computer architecture theory and digital signal processing Covering a large amount of material in a single self contained volume it links VLSI architecture with the algorithms on which the accompanying hardware is based **VLSI Signal Processing** Peter B. Denyer, David Renshaw, 1985 **D2EV**, 1992 **High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures** K. J. Ray Liu, Kung Yao, 1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today's rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures **Signal Processing**

II Hans Wilhelm Schüssler,1983 *High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Systems Design and Applications* K. J. Ray Liu,Kung Yao,1998 *Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 2 Systems Design And Applications* The second volume in a two volume set *High Performance VLSI Signal Processing Innovative Architectures and Algorithms* brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of the latest design tools design environments and implementations of VLSI signal processing systems These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends **VLSI Signal Processing, IV** Howard S. Moscovitz,Kung Yao,Rajeev Jain,1991 **VLSI Signal Processing, VI** IEEE Acoustics, Speech, and Signal Processing Society,1993 **VLSI Design Methodologies for Digital Signal Processing Architectures** Magdy A. Bayoumi,2012-12-06 *Designing VLSI systems* represents a challenging task It is a transfonnation among different specifications corresponding to different levels of design abstraction behavioral stntctural and physical The behavioral level describes the functionality of the design It consists of two components static and dynamic The static component describes operations whereas the dynamic component describes sequencing and timing The structural level contains infonnation about components control and connectivity The physical level describes the constraints that should be imposed on the floor plan the placement of components and the geometry of the design Constraints of area speed and power are also applied at this level To implement such multilevel transfonnation a design methodology should be devised taking into consideration the constraints limitations and properties of each level The mapping process between any of these domains is non isomorphic A single behavioral component may be transfonned into more than one structural component Design methodologies are the most recent evolution in the design automation era which started off with the introduction and subsequent usage of module generation especially for regular structures such as PLA s and memories A design methodology should offer an integrated design system rather than a set of separate unrelated routines and tools A general outline of a desired integrated design system is as follows Decide on a certain unified framework for all design levels Derive a design method based on this framework Create a design environment to implement this design method *VLSI Signal Processing Systems* Earl E. Swartzlander (Jr.),1986 *ICASSP-92* ,1992 *Digital Signal Processing Handbook on CD-ROM* VIJAY MADISETTI,Douglas Williams,1999-02-26 A best seller in its print version this comprehensive CD ROM reference contains unique fully searchable coverage of all major topics in digital signal processing DSP establishing an invaluable time saving resource for the

engineering community Its unique and broad scope includes contributions from all DSP specialties including telecommunications computer engineering acoustics seismic data analysis DSP software and hardware image and video processing remote sensing multimedia applications medical technology radar and sonar applications

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology, 1997 **Computer Systems and Software Engineering** Patrick DeWilde, Joos P.L. Vandewalle, 2012-12-06 Computer Systems and Software Engineering is a compilation of sixteen state of the art lectures and keynote speeches given at the COMPEURO 92 conference The contributions are from leading researchers each of whom gives a new insight into subjects ranging from hardware design through parallelism to computer applications The pragmatic flavour of the contributions makes the book a valuable asset for both researchers and designers alike The book covers the following subjects Hardware Design memory technology logic design algorithms and architecture Parallel Processing programming cellular neural networks and load balancing Software Engineering machine learning logic programming and program correctness Visualization the graphical computer interface

Proceedings ICASSP, Institute of Electrical and Electronics Engineers, Multidimensional Filter Banks and Wavelets Sankar Basu, Bernard C. Levy, 2013-06-29 Multidimensional Filter Banks and Wavelets Research Developments and Applications brings together in one place important contributions and up to date research results in this important area Multidimensional Filter Banks and Wavelets Research Developments and Applications serves as an excellent reference providing insight into some of the most important research issues in the field

Specification And Verification Of Systolic Arrays Magdy A Bayoumi, Nam Ling, 1999-08-05 Circuits and architectures have become more complex in terms of structure interconnection topology and data flow Design correctness has become increasingly significant as errors in design may result in strenuous debugging or even in the repetition of a costly manufacturing process Although circuit simulation has been used traditionally and widely as the technique for checking hardware and architectural designs it does not guarantee the conformity of designs to specifications Formal methods therefore become vital in guaranteeing the correctness of designs and have thus received a significant amount of attention in the CAD industry today This book presents a formal method for specifying and verifying the correctness of systolic array designs Such architectures are commonly found in the form of accelerators for digital signal image and video processing These arrays can be quite complicated in topology and data flow In the book a formalism called STA is defined for these kinds of dynamic environments with a survey of related techniques A framework for specification and verification is established Formal verification techniques to check the correctness of the systolic networks with respect to the algorithmic level specifications are explained The book also presents a Prolog based formal design verifier named VSTA developed to automate the verification process as using a general purpose theorem prover is usually extremely time consuming Several application examples are included in the book to illustrate how formal techniques and the verifier can be used to automate proofs

Efficient Algorithms for Discrete Wavelet Transform K K

Shukla, Arvind K. Tiwari, 2013-01-26 Due to its inherent time scale locality characteristics the discrete wavelet transform DWT has received considerable attention in signal image processing Wavelet transforms have excellent energy compaction characteristics and can provide perfect reconstruction The shifting translation and scaling dilation are unique to wavelets Orthogonality of wavelets with respect to dilations leads to multigrid representation As the computation of DWT involves filtering an efficient filtering process is essential in DWT hardware implementation In the multistage DWT coefficients are calculated recursively and in addition to the wavelet decomposition stage extra space is required to store the intermediate coefficients Hence the overall performance depends significantly on the precision of the intermediate DWT coefficients This work presents new implementation techniques of DWT that are efficient in terms of computation storage and with better signal to noise ratio in the reconstructed signal

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