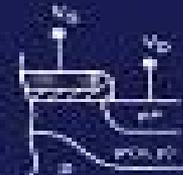
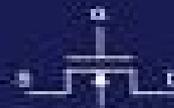


TECHNOLOGY CAD

COMPUTER SIMULATION OF IC PROCESSES AND DEVICES



Robert W. Dutton
Zhiping Yu

Technology Cad Computer Simulation Of Ic Processes And Devices

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Integrated Power Devices and TCAD Simulation Yue Fu, Zhanming Li, Wai Tung Ng, Johnny K.O. Sin, 2017-12-19 From power electronics to power integrated circuits PICs smart power technologies devices and beyond Integrated Power Devices and TCAD Simulation provides a complete picture of the power management and semiconductor industry An essential reference for power device engineering students and professionals the book not only describes the physics inside integrated power semiconductor devices such lateral double diffused metal oxide semiconductor field effect transistors LDMOSFETs lateral insulated gate bipolar transistors LIGBTs and super junction LDMOSFETs but also delivers a simple introduction to power management systems Instead of abstract theoretical treatments and daunting equations the text uses technology computer aided design TCAD simulation examples to explain the design of integrated power semiconductor devices It also explores next generation power devices such as gallium nitride power high electron mobility transistors GaN power HEMTs Including a virtual process flow for smart PIC technology as well as a hard to find technology development organization chart Integrated Power Devices and TCAD Simulation gives students and junior engineers a head start in the field of power semiconductor devices while helping to fill the gap between power device engineering and power management systems

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Internet Accessible Remote Laboratories: Scalable E-Learning Tools for Engineering and Science Disciplines Azad, Abul K.M., Auer, Michael E., Harward, V. Judson, 2011-11-30 This book presents current developments in the multidisciplinary creation of Internet accessible remote laboratories offering perspectives on teaching with online laboratories pedagogical design system architectures for remote laboratories future trends and policy issues in the use of remote laboratories Provided by publisher

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Process and Device Modeling for Integrated Circuit Design F. van de Wiele, W.L. Engl, P. Jespers, 1977-11-30 An Advanced Study Institute on process and device modeling for integrated circuit design was held in Louvain la Neuve Belgium on July 19 29 1977 under the auspices of the Scientific Affairs Division of NATO The Institute was organized by a scientific organizing committee consisting of Professor F Van de Wiele of the Universite Catholique de Louvain Professor W L Engl of the Technische Hochschule Aachen and Professor P Jespers of the Universite Catholique de Louvain This book represents the contributions of the lecturers at the Institute and the chapters present a concise treatment of a very timely subject namely process and device modeling for integrated circuit design The organization of the book parallels the program at the Institute with an introduction comprised of a review of modeling and basic semiconductor physics This is followed by the chapters devoted to basic technologies modeling of bipolar and MoS devices The last chapter of the book presents the specific topic of process modeling The subject matter of this book is suitable for a wide range of interests from the advanced student through the practising physicist and engineer to the research worker Although a novice may find some difficulty with the mathematical development he can acquire a perspective into the field of process and device modeling for integrated circuit design with this book Likewise portions of this book may be used as a textbook since the chapters are instructional and self contained

Formal Semantics for VHDL Carlos Delgado Kloos, P. Breuer, 1995-02-28 Research papers from a January 1994 workshop put forth a cohesive set of semantics for the VHDL language and describe semantics based on Petri nets as target language higher order logic functional concepts and evolving algebras Quotes from Lewis Carroll's Alice Through the Looking Glass introduce each paper For researchers in formal methods and advanced students No index Annotation copyright by Book News Inc Portland OR

Digital Timing Macromodeling for VLSI Design Verification Jeong-Taek Kong, David V. Overhauser, 1995-05-31 Digital Timing Macromodeling for VLSI Design Verification first of all provides an extensive history of the development of simulation techniques It presents detailed discussion of the various techniques implemented in circuit timing fast timing switch level timing switch level and gate level simulation It also discusses mixed mode simulation and interconnection analysis methods The review in Chapter 2 gives an understanding of the advantages and disadvantages of the many techniques applied in modern digital macromodels The book also presents a wide variety of techniques for performing nonlinear macromodeling of digital MOS subcircuits which address a large number of shortcomings in existing digital MOS macromodels Specifically the techniques address the device model detail transistor coupling capacitance

effective channel length modulation series transistor reduction effective transconductance input terminal dependence gate parasitic capacitance the body effect the impact of parasitic RC interconnects and the effect of transmission gates The techniques address major sources of errors in existing macromodeling techniques which must be addressed if macromodeling is to be accepted in commercial CAD tools by chip designers The techniques presented in Chapters 4 6 can be implemented in other macromodels and are demonstrated using the macromodel presented in Chapter 3 The new techniques are validated over an extremely wide range of operating conditions much wider than has been presented for previous macromodels thus demonstrating the wide range of applicability of these techniques

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