Ramayya Kumar
Thomas Kropf (Eds.)

Theorem Provers in Circuit Design

Theory, Practice and Experience

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Proceedings

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Theorem Provers Circuit Design

Victoria Stavridou

Theorem Provers Circuit Design:

    Theorem Provers in Circuit Design Victoria Stavridou,Tom F. Melham,Raymond T. Boute,1992 The papers in this volume address the role of mechanized theorem proving technology in the design of digital systems The primary focus is on the practical application of theorem provers to digital design rather than on theoretical foundations The diverse contributions include invited papers by the leading researchers Gordon and Hunt as well as technical contributions by many other prominent researchers in the field of machine assisted hardware verification This side of the Proceedings reflects current research activity and the section containing tutorial papers on several influential theorem provers serves as an introduction to this exciting field   *Theorem Provers in Circuit Design* Ramayya Kumar,Thomas Kropf,1995-03-06 This two volume set contains papers presented at the International Conference on Computational Engineering Science ICES 95 held in Mauna Lani Hawaii from 30 July to 3 August 1995 The contributions capture the state of the science in computational modeling and simulation in a variety of engineering disciplines civil mechanical aerospace materials and electronics engineering

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    Theorem Provers in Circuit Design Ramayya Kumar,Thomas Kropf,2014-01-15   Formal Methods in Circuit Design Victoria Stavridou,1993-07-22 Graduate level account of hardware verification and algebraic specification   *Higher Order Logic Theorem Proving and Its Applications* Thomas F. Melham,1994-09-07 This volume presents the proceedings of the 7th International Workshop on Higher Order Logic Theorem Proving and Its Applications held in Valetta Malta in September 1994 Besides 3 invited papers the proceedings contains 27 refereed papers selected from 42 submissions In total the book presents many new results by leading researchers working on the design and applications of theorem provers for higher order logic In particular this book gives a thorough state of the art report on applications of the HOL system one of the most widely used theorem provers for higher order logic   Designing Correct Circuits Geraint Jones,Mary Sheeran,2013-12-14 These proceedings contain the papers presented at a workshop on Designing Correct Circuits jointly organised by the Universities of Oxford and Glasgow and held in Oxford on 26 28 September 1990 There is a growing interest in the application to hardware design of the techniques of software engineering As the complexity of hardware systems grows and as the cost both in money and time of making design errors becomes more apparent so there is an eagerness to build on the success of mathematical techniques in program develop ment The harsher constraints on hardware designers mean both that there is a greater need for good abstractions and rigorous assurances of the trustworthyness of designs and also that there is greater reason to expect that these benefits can be realised The papers presented at this workshop consider the application of mathematics to hardware design at several different levels of abstraction At the lowest level of this spectrum Zhou and Hoare show how to describe and reason about synchronous switching circuits using UNilY a formalism that was developed for reasoning about parallel programs Aagaard and Leeser use standard mathematical tech niques to prove correct their

implementation of an algorithm for Boolean simplification The circuits generated by their formal synthesis system are thus correct by construction Thuau and Pilaud show how the declarative language LUSTRE which was designed for program ming real time systems can be used to specify synchronous circuits   *Introduction to Formal Hardware Verification* Thomas Kropf,2013-03-09 Hardware verification is a hot topic in circuit and system design due to rising circuit complexity This advanced textbook presents an almost complete overview of techniques for hardware verification It covers all approaches used in existing tools such as binary and word level decision diagrams symbolic methods for equivalence checking and temporal logic model checking and introduces the use of higher order logic theorem proving for verifying circuit correctness It enables the reader to understand the advantages and limitations of each technique Each chapter contains an introduction and a summary as well as a section for the advanced reader Thus a broad audience is addressed from beginners in system design to experts   **Theorem Proving in Higher Order Logics** Joakim von Wright,Jim Grundy,John Harrison,1996-08-07 This book constitutes the refereed proceedings of the 9th International Conference on Theorem Proving in Higher Order Logics TPHOL 96 held in Turku Finland in August 1996 The 27 revised full papers included together with one invited paper were carefully selected from a total of 46 submissions The topics addressed are theorem proving technology proof automation and decision procedures mechanized theorem proving extensions of higher order logics integration of external tools novel applications and others All in all the volume is an up to date report on the state of the art in this increasingly active field   **NASA Contractor Report** ,1991   *The Foundation of a Generic Theorem Prover* Lawrence C. Paulson,1988 Examples illustrate use of this meta logic to formalize logics and proofs Axioms for first order logic are shown sound and complete Backwards proof is formalized by meta reasoning about object level entailment Higher order logic has several practical advantages over other meta logics Many proof techniques are known such as Huet s higher order unification algorithm   *Verification of the FtCayuga Fault-tolerant Microprocessor System: A case study in theorem prover-based verification* Mandayam Srivas,1991   **1995 IEEE International Conference on Computer Design** ,1995   *Verification of the FtCayuga Fault-tolerant Microprocessor System. Volume 1: A Case Study in Theorem Prover-based Verification* Mandayam Srivas,1991   Correct Hardware Design and Verification Methods ,1995   Proceedings ,1981   **Theorem Proving with the Real Numbers** John Robert Harrison,1996 Abstract This thesis discusses the use of the real numbers in theorem proving Typically theorem provers only support a few discrete datatypes such as the natural numbers However the availability of the real numbers opens up many interesting and important application areas such as the verification of floating point hardware and hybrid systems It also allows the formalization of many more branches of classical mathematics which is particularly relevant for attempts to inject more rigour into computer algebra systems Our work is conducted in a version of the HOL theorem prover We describe the rigorous definitional construction of the real numbers using a new version of Cantor s method and the formalization of a significant portion of real analysis We also describe an advanced derived decision

procedure for the Tarski subset of real algebra as well as some more modest but practically useful tools for automating explicit calculations and routine linear arithmetic reasoning Finally we consider in more detail two interesting application areas We discuss the desirability of combining the rigour of theorem provers with the power and convenience of computer algebra systems and explain a method we have used in practice to achieve this We then move on to the verification of floating point hardware After a careful discussion of possible correctness specifications we report on two case studies one involving a transcendental function We aim to show that a theory of real numbers is useful in practice and interesting in theory and that the LCF style of theorem proving is well suited to the kind of work we describe We hope also to convince the reader that the kind of mathematics needed for applications is well within the abilities of current theorem proving technology *Designing Correct Circuits* Jørgen Staunstrup,Robin Sharp,1992 The papers compiled in this volume cover a wide range of topics within the general area of provably correct design including proofs of fundamental results post hoc formal verification of designs and design techniques where correctness is ensured by construction The papers are unique in that they demonstrate the use of design techniques for designing real chips The book should thus not be missed by students and researchers interested in the deployment of formally based techniques on real problems within a great diversity of application areas Application areas treated within the volume range from packet routers arbiters multiprocessors serial parallel converters and CPUs to arithmetic units and counters A significant group of the papers is concerned with the special requirements of asynchronous and delay insensitive design A Framework for the Specification, Design, and Verification of Pipelines with Structural Hazards Mark Aagaard,1995 **Proceedings of the 1991 International Workshop on the HOL Theorem Proving System and Its Applications, August 28-30, 1991, Davis, California** Myla Archer,1992

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