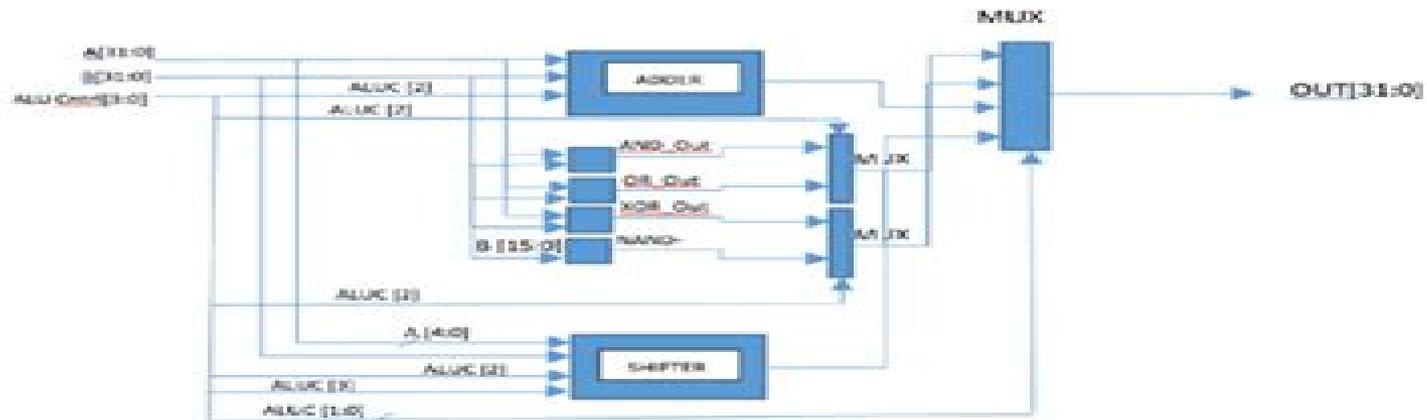


VLSI Architectures

Assignment 1 - Design of ALU

Yeshvanth M, 2023HT01517

High-level design in question



Solution

AND Gate

```

////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////
// 32-bit AND Gate used in ALU //
: module AND
(
    input[31:0]  A_i,    // Input 1
    input[31:0]  B_i,    // Input 2
    output[31:0] AND_o  // AND result
);

    assign AND_o = A_i & B_i; // Behavioral modelling of AND gate
: endmodule

```

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SA Dillow



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High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today's rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures **VLSI Algorithms and Architectures** N. Ranganathan,1993 The second volume of a two volume set the first is VLSI Algorithms and Architectures Fundamentals introducing basic approaches to the design of VLSI algorithms and architectures and exposing various computationally intensive problems requiring VLSI solutions The present volume addresses more advanced topics
VLSI Algorithms and Architectures Fillia Makedon,Kurt Mehlhorn,T. Papatheodorou,2014-01-15 [Design of VLSI Algorithms and Architectures for Digital Image and Signal Processing](#)
Krzysztof Wojciech Przytula,1985 [Algorithms for VLSI Physical Design Automation](#) Naveed A. Sherwani,2012-12-06 Algorithms for VLSI Physical Design Automation Second Edition is a core reference text for graduate students and CAD professionals Based on the very successful First Edition it provides a comprehensive treatment of the principles and algorithms of VLSI physical design presenting the concepts and algorithms in an intuitive manner Each chapter contains 3-4 algorithms that are discussed in detail Additional algorithms are presented in a somewhat shorter format References to advanced algorithms are presented at the end of each chapter Algorithms for VLSI Physical Design Automation covers all aspects of physical design In 1992 when the First Edition was published the largest available microprocessor had one million

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and fabrication design disciplines and in the integrated circuit industry itself Within the last few years a community of researchers is growing to address issues closer to computer science the relationship between computing structures and the physical structures that implement them the specification and verification of computational processes implemented in VLSI the use of massively parallel computing made possible by VLSI the design of special purpose computing architectures and the changes in general purpose computer architecture that VLSI makes possible It is likely that the future exploitation of VLSI technology depends as much on structural and design innovations as on advances in fabrication technology The book is divided into nine sections Invited Papers Six distinguished researchers from industry and academia presented invited papers Models of Computation The papers in this section deal with abstracting the properties of VLSI circuits into models that can be used to analyze the chip area time or energy required for a particular computation

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