

SystemC Kernel Extensions for Heterogeneous System Modeling

A Framework for Multi-MoC Modeling & Simulation

By
Hiren D. Patel and Sandeep K. Shukla

Kluwer Academic Publishers

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Huangqi Zhang



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Synthesis of Embedded Software Sandeep Kumar Shukla, Jean-Pierre Talpin, 2010-08-05 Embedded software is ubiquitous today There are millions of lines of embedded code in smart phones and even more in systems responsible for automotive control avionics control weapons control and space missions Some of these are safety critical systems whose correctness timely response and reliability are of paramount importance These requirements pose new challenges to system designers This necessitates that a proper design science based on constructive correctness be developed Correct by construction design and synthesis of embedded software is done in a way so that post development verification is minimized and correct operation of embedded systems is maximized This book presents the state of the art in the design of safety critical embedded software It introduced readers to three major approaches to specification driven embedded software synthesis construction synchronous programming based approaches models of computation based approaches and an approach based on concurrent programming with a co design focused language It is an invaluable reference for practitioners and researchers concerned with improving the product development life cycle

Metamodeling-driven IP Reuse for SoC Integration and Microprocessor Design Deepak A. Mathaikutty, Sandeep Shukla, 2009 This cutting edge resource offers you an in depth understanding of metamodeling approaches for the reuse of intellectual properties IPs in the form of reusable design or verification components The book covers the essential issues associated with fast and effective integration of reusable design components into a system on a chip SoC to achieve faster design turn around time Moreover it addresses key factors related to the use of reusable verification IPs for a write once use many times verification strategy another effective approach that can attain a faster product design cycle

Model Driven Engineering Languages and Systems Dorina C. Petriu, Nicolas Rouquette, Oystein Haugen, 2010-09-22 The MODELS series of conferences is the premier venue for the exchange of novel technical ideas and experiences focusing on a very important new technical discipline model driven software and systems engineering The expansion of this discipline is a direct consequence of the increasing significance and success of model based

methods in practice Numerous efforts resulted in the invention of concepts languages and tools for the definition analysis transformation and verification of domain specific modeling languages and general purpose modeling language standards as well as their use for software and systems engineering

MODELS 2010 the 13th edition of the conference series took place in Oslo Norway October 3-8 2010 along with numerous satellite workshops symposia and tutorials The conference was fortunate to have three prominent keynote speakers Ole Lehrmann Madsen Aarhus University Denmark Edward A Lee UC Berkeley USA and Pamela Zave AT T Laboratories USA To provide a broader forum for reporting on scientific progress as well as on experience stemming from practical applications of model based methods the 2010 conference accepted submissions in two distinct tracks Foundations and Applications The primary objective of the first track is to present new research results dedicated to advancing the state of the art of the discipline whereas the second aims to provide a realistic and verifiable picture of the current state the practice of model based engineering so that the broader community could be better informed of the capabilities and successes of this relatively young discipline This volume contains the final version of the papers accepted for presentation at the conference from both tracks

Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15 Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts It targets heterogeneous systems by combining finite state machines FSM with synchronous dataflow graphs SDFG FSMs are connected in the same way as in Argos a Statecharts variant with purely synchronous semantics using three operators synchronous parallel refinement and hiding The fourth operator called asynchronous parallel is introduced in DFCharts to connect FSMs with SDFGs In the formal semantics of DFCharts the operation of an SDFG is represented as an FSM Using this representation SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM This allows system properties to be verified globally The practical application of DFCharts has been demonstrated by linking it to widely used system level languages Java Esterel and SystemC

Embedded Systems Specification and Design Languages Eugenio Villar, 2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages

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The British National Bibliography Arthur James Wells, 2004 **CASES ...**, 2006 American Book

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Table of Contents Systemc Kernel Extensions For Heterogenous System Modeling

1. Understanding the eBook Systemc Kernel Extensions For Heterogenous System Modeling
 - The Rise of Digital Reading Systemc Kernel Extensions For Heterogenous System Modeling
 - Advantages of eBooks Over Traditional Books
2. Identifying Systemc Kernel Extensions For Heterogenous System Modeling
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Systemc Kernel Extensions For Heterogenous System Modeling
 - User-Friendly Interface
4. Exploring eBook Recommendations from Systemc Kernel Extensions For Heterogenous System Modeling
 - Personalized Recommendations
 - Systemc Kernel Extensions For Heterogenous System Modeling User Reviews and Ratings

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- Systemc Kernel Extensions For Heterogenous System Modeling and Bestseller Lists
- 5. Accessing Systemc Kernel Extensions For Heterogenous System Modeling Free and Paid eBooks
 - Systemc Kernel Extensions For Heterogenous System Modeling Public Domain eBooks
 - Systemc Kernel Extensions For Heterogenous System Modeling eBook Subscription Services
 - Systemc Kernel Extensions For Heterogenous System Modeling Budget-Friendly Options
- 6. Navigating Systemc Kernel Extensions For Heterogenous System Modeling eBook Formats
 - ePub, PDF, MOBI, and More
 - Systemc Kernel Extensions For Heterogenous System Modeling Compatibility with Devices
 - Systemc Kernel Extensions For Heterogenous System Modeling Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Systemc Kernel Extensions For Heterogenous System Modeling
 - Highlighting and Note-Taking Systemc Kernel Extensions For Heterogenous System Modeling
 - Interactive Elements Systemc Kernel Extensions For Heterogenous System Modeling
- 8. Staying Engaged with Systemc Kernel Extensions For Heterogenous System Modeling
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Systemc Kernel Extensions For Heterogenous System Modeling
- 9. Balancing eBooks and Physical Books Systemc Kernel Extensions For Heterogenous System Modeling
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Systemc Kernel Extensions For Heterogenous System Modeling
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Systemc Kernel Extensions For Heterogenous System Modeling
 - Setting Reading Goals Systemc Kernel Extensions For Heterogenous System Modeling
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Systemc Kernel Extensions For Heterogenous System Modeling
 - Fact-Checking eBook Content of Systemc Kernel Extensions For Heterogenous System Modeling
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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