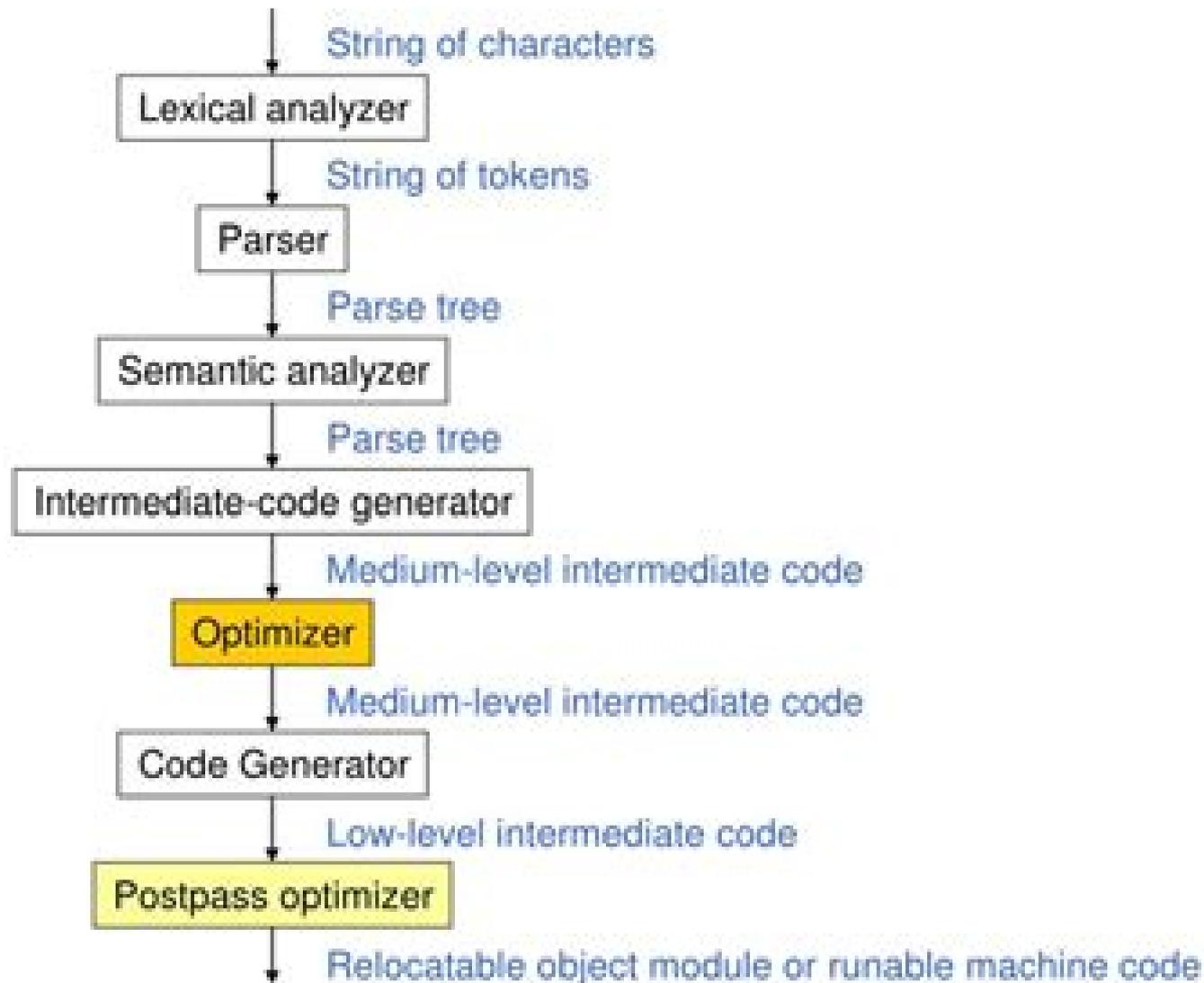


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Design and Implementation of an Optimizing Compiler for a Single Assignment Language Peter Schnorf, Mahadevan Ganapathi, Stanford University. Computer Systems Laboratory, John L. Hennessy, 1990 *Languages and Compilers for Parallel Computing* Henry Gordon Dietz, 2003-05-20 This book constitutes the thoroughly refereed post proceedings of the 14th International Workshop on Languages and Compilers for Parallel Computing LCPC 2001 held in Lexington KY USA in August 1 3 2001 The 28 revised full papers presented were carefully selected during two rounds of reviewing and improvement All current issues in parallel processing are addressed in particular compiler optimization HP Java programming power aware parallel architectures high performance applications power management of mobile computers data distribution shared memory systems load balancing garbage collection parallel components job scheduling dynamic parallelization cache optimization specification and dataflow analysis Advanced Compiler Design Implementation Steven Muchnick, 1997-08 Computer professionals who need to understand advanced techniques for designing efficient compilers will need this book It provides complete coverage of advanced issues in the design of compilers with a major emphasis on creating highly optimizing scalar compilers It includes interviews and printed documentation from designers and implementors of real world compilation systems *The Design and Implementation of the Self Compiler, an Optimizing Compiler for Object-oriented Programming Languages* Craig Chambers, 1992 **STAN CS 92 1420** Craig Chambers, 1992 **Register Allocation in Optimizing Compilers** Bruce W. Leverett, 1983

Optimizing Compilers for Modern Architectures: A Dependence-Based Approach Randy Allen, Ken Kennedy, 2001-10 Modern computer architectures designed with high performance microprocessors offer tremendous potential gains in performance over previous designs Yet their very complexity makes it increasingly difficult to produce efficient code and to realize their full potential This landmark text from two leaders in the field focuses on the pivotal role that compilers can play in addressing this critical issue The basis for all the methods presented in this book is data dependence a fundamental compiler analysis tool for optimizing programs on high performance microprocessors and parallel architectures It enables compiler designers to write compilers that automatically transform simple sequential programs into forms that can exploit special features of these modern architectures The text provides a broad introduction to data dependence to the many transformation strategies it supports and to its applications to important optimization problems such

as parallelization compiler memory hierarchy management and instruction scheduling The authors demonstrate the importance and wide applicability of dependence based compiler optimizations and give the compiler writer the basics needed to understand and implement them They also offer cookbook explanations for transforming applications by hand to computational scientists and engineers who are driven to obtain the best possible performance of their complex applications The approaches presented are based on research conducted over the past two decades emphasizing the strategies implemented in research prototypes at Rice University and in several associated commercial systems Randy Allen and Ken Kennedy have provided an indispensable resource for researchers practicing professionals and graduate students engaged in designing and optimizing compilers for modern computer architectures Offers a guide to the simple practical algorithms and approaches that are most effective in real world high performance microprocessor and parallel systems Demonstrates each transformation in worked examples Examines how two case study compilers implement the theories and practices described in each chapter Presents the most complete treatment of memory hierarchy issues of any compiler text Illustrates ordering relationships with dependence graphs throughout the book Applies the techniques to a variety of languages including Fortran 77 C hardware definition languages Fortran 90 and High Performance Fortran Provides extensive references to the most sophisticated algorithms known in research

A Retargetable C Compiler Christopher W. Fraser, David R. Hanson, 1995 This book brings a unique treatment of compiler design to the professional who seeks an in depth examination of a real world compiler Chris Fraser of AT a target independent front end and three target dependent back ends are packaged as a single program designed to run on three different platforms Rather than transfer code into a text file the book and the compiler itself are generated from a single source to ensure accuracy

Advances in Real-Time Systems Samarjit Chakraborty, Jörg Eberspächer, 2012-02-09 This volume contains the lectures given in honor to Georg F rber as tribute to his contributions in the area of real time and embedded systems The chapters of many leading scientists cover a wide range of aspects like robot or automotive vision systems or medical aspects

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