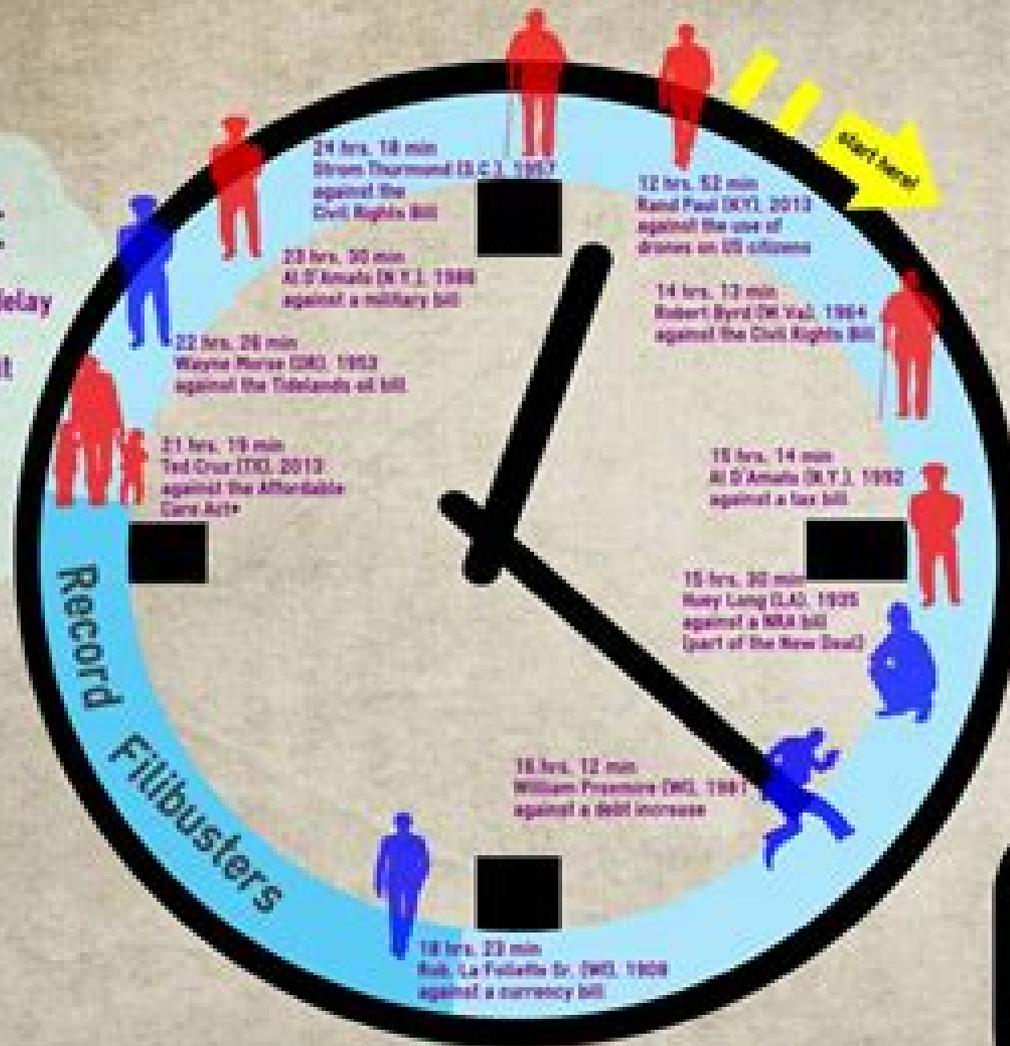


# Filibusters in the United States

## definition:

"any attempt to block or delay Senate action on a bill or other matter by debating it at length, by offering numerous procedural motions, or by any other delaying or obstructive actions." \*\*



debate - if 3/5 of Senators agree via vote, a bill can be made "filibuster-proof". Debate was created in 1817 but required 2/3 until 1975

1841  
first filibuster

24 HRS

in "My Smith Goes to Washington" Jefferson Smith (Jimmy Stewart) Willard Dan, 1939

\*\*not an official filibuster  
<https://www.senate.gov/reference/glossary/terms/filibuster.htm> History / Culture / Senate, [https://www.senate.gov/legislative/History/Procedures/Filibuster\\_culture.htm](https://www.senate.gov/legislative/History/Procedures/Filibuster_culture.htm)  
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 Prepared by Edwin Jung, AP US Gov and Politics, October 9, 2013

# The Filibuster

**J Rink**



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web as an alternative you can also write your simulation stimulus using generic vhdl code called a testbench the testbench codes gets compiled along with the design so that the stimulus is also in the compiled simulation model ieee came along and created additional libraries that are supported by almost all vhdl compilers specific silicon

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we

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