



Synthesis Of Arithmetic Circuits

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VLSI-SoC: Design and Engineering of Electronics Systems Based on New Computing Paradigms Nicola Bombieri,Graziano Pravadelli,Masahiro Fujita,Todd Austin,Ricardo Reis,2019-06-25 This book contains extended and revised versions of the best papers presented at the 26th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2018 held in Verona Italy in October 2018 The 13 full papers included in this volume were carefully reviewed and selected from the 27 papers out of 106 submissions

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