

STRESS-INDUCED PHENOMENA IN METALLIZATION

Sixth International Workshop on Stress-Induced
Phenomena in Metallization

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Stress-Induced Phenomena in Metallization Paul S. Ho, Shinichi Ogawa, Ehrenfried Zschech, 2009-07-02 This symposium focuses on new developments in the fields of experimental and theoretical nuclear physics including nuclear dynamics nuclear structure including hypernuclei nuclear matter nuclear astrophysics applications The participants also reported on the innovative instrumentation including future large scale facility both in Japan and Italy that will be essential for future studies

Stress-Induced Phenomena in Metallization, 1992 *Stress-Induced Phenomena in Metallization* Shinichi Ogawa, Paul S. Ho, Ehrenfried Zschech, 2007-11-13 Kyoto Japan 4 7 April 2007

Stress-Induced Phenomena in Metallization Ehrenfried Zschech, 2006-02-16 These proceedings present current research on issues related to stress induced phenomena in on chip metal interconnects and solder joints The volume will appeal to scientists engineers graduate students interested in research and development of microelectronic devices as well as technology integration and semiconductor industry professionals and equipment suppliers

Stress-induced Phenomena in Metallization American Institute of Physics, 1992

Stress-Induced Phenomena in Metallization Ehrenfried Zschech, Shinichi Ogawa, Paul S. Ho, 2010-12-20 One current challenge to micro and nanoelectronics is the understanding of stress related phenomena in metallization Stresses arising in on chip and 3D metal interconnects and in the surrounding materials due to thermal mismatch microstructure changes or process integration as well as electromigration can lead to degradation and failure of microelectronic products The implementation of low dielectric constant materials into the inlaid copper backend of line process has brought new challenges for process integration and reliability Understanding stress related phenomena in new materials used for 3D integration and packaging particularly using through silicon vias and microbumps is critical for future microelectronic products The Proceedings summarize new research results and advances in basic understanding of stress induced phenomena in metallization In addition to experimental studies modelling and simulation capabilities are demonstrated to evaluate the effect of stress on product performance and reliability Stress related phenomena in 3D IC interconnects are covered too

Stress Induced Phenomena in Metallization Shefford P. Baker, 2002-04-09 Tiny metal structures less than a millionth of a meter across are critical building blocks in a number of high tech devices such as computer chips These metallizations are subjected to extreme conditions of temperature electric current density and mechanical load which may cause the device they are in to fail This book contains research papers on these metallizations and on the reliability problems associated with them The papers were peer reviewed for these proceedings

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in Metallization P. S. Ho, 2004-12-14 Continuing the spirit of the previous workshops the proceedings contain new research results and advances in basic understanding of stress induced phenomena in metallization The current technology drive to implement low dielectric constant materials into copper metallization has brought new and significant challenges in process integration and reliability Stresses arising in metallizations and surrounding dielectric structures due to thermal mismatch electromigration microstructure changes or process integration can lead to damage and failure of interconnect structures Understanding stress related phenomena in new materials and structures becomes critical for reliability improvement and metallization development This is reflected in the papers included in the proceedings which report results on electromigration thermal stresses and void formation in copper low k interconnect structures The book also includes new results on fracture of low k dielectric structures an important research area for reliability and integration of copper metallization Stress-Induced Phenomena in Metallization Ehrenfried Zschech, Karen Maex, Paul S. Ho, Hisao

Kawasaki, Tomoji Nakamura, 2006-02-16 These proceedings present current research on issues related to stress induced phenomena in on chip metal interconnects and solder joints The volume will appeal to scientists engineers graduate students interested in research and development of microelectronic devices as well as technology integration and semiconductor industry professionals and equipment suppliers *Stress-induced Phenomena in Metallization*, 2007 **Stress-induced Phenomena in Metallization** Paul S. Ho, 1996 **Stress-induced Phenomena in Metallization**, 2004

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addition to semiconductor devices Problems related to mass transport in these devices have become increasingly important in some cases more so than those in semiconductor devices Several papers were presented to discuss the similarities and differences of these phenomena and their mechanisms focusing on the questions concerning why and how atoms migrate and what are the effects of the migration

Electromigration in ULSI Interconnections Cher Ming Tan,2010 *Electromigration in ULSI Interconnections* provides a comprehensive description of the electro migration in integrated circuits It is intended for both beginner and advanced readers on electro migration in ULSI interconnections It begins with the basic knowledge required for a detailed study on electro migration and examines the various interconnected systems and their evolution employed in integrated circuit technology The subsequent chapters provide a detailed description of the physics of electro migration in both Al and Cu based Interconnections in the form of theoretical experimental and numerical modeling studies The differences in the electro migration of Al and Cu based interconnections and the corresponding underlying physical mechanisms for these differences are explained The test structures testing methodology failure analysis methodology and statistical analysis of the test data for the experimental studies on electro migration are presented in a concise and rigorous manner Methods of numerical modeling for the interconnect electro migration and their applications to the understanding of electro migration physics are described in detail with the aspects of material properties interconnection design and interconnect process parameters on the electro migration performances of interconnects in ULSI further elaborated upon Finally the extension of the studies to narrow interconnections is introduced and future challenges on the study of electro migration are outlined and discussed

Electromigration in Metals Paul S. Ho,Chao-Kun Hu,Martin Gall,Valeriy Sukharev,2022-05-12 Learn to assess electromigration reliability and design more resilient chips in this comprehensive and practical resource Beginning with fundamental physics and building to advanced methodologies this book enables the reader to develop highly reliable on chip wiring stacks and power grids Through a detailed review on the role of microstructure interfaces and processing on electromigration reliability as well as characterisation testing and analysis the book follows the development of on chip interconnects from microscale to nanoscale Practical modeling methodologies for statistical analysis from simple 1D approximation to complex 3D description can be used for step by step development of reliable on chip wiring stacks and industrial grade power ground grids This is an ideal resource for materials scientists and reliability and chip design engineers

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In a digital era where connections and knowledge reign supreme, the enchanting power of language has been apparent than ever. Its ability to stir emotions, provoke thought, and instigate transformation is really remarkable. This extraordinary book, aptly titled "**Stress Induced Phenomena In Metallization**," compiled by a very acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound impact on our existence. Throughout this critique, we shall delve into the book's central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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