Matrix Computations on Systolic-Type Arrays

Jaime H. Moreno
Tomás Lang

Systolic Computations

Michal Chytil,M.P. Chytil,V. Koubek

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    Systolic Computations M.A. Frumkin,2012-12-06 This monograph is devoted to a new method of parallel computing which uses VLSI technology in an effcient manner By this method data are fed to the cells of a systolic processor and results are obtained instantly Some theoretical and algorithmic questions which arise in the design of hardware and software for systolic processing are considered Special attention is devoted to the complexity of VLSI complexity of algorithms parallel algorithms relations between graphs of algorithms and graphs of processors parallel programming languages and the use of systolic algorithms for vector programming The book is unique for its inclusion of a library of systolic algorithms for solving problems from twelve branches of computer science and will be useful for designers of hardware and software for parallel processing   Concurrent Computations Stuart K. Tewksbury,Bradley Dickinson,Stuart C. Schwartz,2012-12-06 The 1987 Princeton Workshop on Algorithm Architecture and Technology Issues for Models of Concurrent Computation was organized as an interdisciplinary work shop emphasizing current research directions toward concurrent computing systems With participants from several different fields of specialization the workshop cov ered a wide variety of topics though by no means a complete cross section of issues in this rapidly moving field The papers included in this book were prepared for the workshop and taken together provide a view of the broad range of issues and alternative directions being explored To organize the various papers the book has been divided into five parts Part I considers new technology directions Part II emphasizes underlying theoretical issues Communication issues which are ad dressed in the majority of papers are specifically highlighted in Part III Part IV includes papers stressing the fault tolerance and reliability of systems Finally Part V includes systems oriented papers where the system ranges from VLSI circuits through powerful parallel computers Much of the initial planning of the workshop was completed through an informal AT T Bell Laboratories group consisting of Mehdi Hatamian Vijay Kumar Adri aan Ligtenberg Sailesh Rao P Subrahmanyam and myself We are grateful to Stuart Schwartz both for the support of Princeton University and for his orga nizing local arrangements for the workshop and to the members of the organizing committee whose recommendations for participants and discussion topics were par ticularly helpful A Rosenberg and A T   Transputing '91 Peter Welch,Toshiyasu L. Kunii,A. Bakkers,1991 Transputers constitute a revolutionary category of microprocessors for parallel processing which have become market leaders in 32 bit RISC architectures The wide range of applications has caused a multitude of activities of user groups in all major countries as well as regional activities on four continents For the first time the collaboration of all these user groups has let to the organization of a world conference Transputing 91   **Trace Theory and Systolic Computations** Martin Rem,1987   *Bit-level Systolic Arrays* Anthony John De Groot,1989   *The Application of a Sequence Notation to the Design of Systolic Computations* Rami Melhem,PITTSBURGH UNIV PA INST FOR COMPUTATIONAL MATHEMATICS AND APPLICATIONS.,1985 The sequence notation suggested in an earlier work provides a tool for the clear and precise

specification of systolic computations Namely it separates the static and dynamic levels of the specification At the static level the topology of the network and the function of each cell are described by a system of causal equations on sequences and at the dynamic level the data flow is described by the elements of the individual sequences This paper describes a method for the transformation of a given algorithm into a system of causal sequence equations input output description which specifies a systolic computation The basic idea of the method is to pack arrays of variables along one or more dimensions into sequences Doing this however may result in a system of equations that is not causal and hence a transformation of indices in the original algorithm may be essential in order to guarantee causality the positive increment of time   *Supercomputers and Parallel Computation* D. J. Paddon,1984   **Area-efficient VLSI Computation** Charles Eric Leiserson,1983

     Mathematical Foundations of Computer Science ,1984   **1985 International Symposium on VLSI Technology, Systems, and Applications** ,1985   **TENCON '93** ,1993   **Mathematical Foundations of Computer Science 1984** Michal Chytil,M.P. Chytil,V. Koubek,1984-08   **IEEE Proceedings of the Southeastcon** ,1985   **Performance Prediction for Mapped Parallel Programs** Bernd Heinrich Stramm,1993   **High Speed Computing** David Paul Casasent,1988   Energy and Technology Review ,1989   **Large-scale Matrix Problems and the Numerical Solution of Partial Differential Equations** John E. Gilbert,Donald Kershaw,1994 This book contains contributions from the invited speakers to the fifth S E R C Summer School in Numerical Analysis which was held at Lancaster University from 19th to 31st July 1992 The expositions were at a level which could be understood by post graduate research students yet would be advanced enough to stimulate established researchers The book should therefore be useful to a wide class of readers The topics which are covered include some of the most important areas of current research in numerical analysis The contributors are Jesse L Barlow Pennsylvania State University Professor Jack Dongarra Oak Ridge National Library Professor Howard C Elman Institute for Advanced Computer Studies Maryland Professor Randolph E Bank University of California Professor J W Jerome Northwestern University and Professor Maurizio Pandolfi Politecnico di Torino Italy   *Proceedings of the ... Bioengineering Conference* ,1995   **Proceedings of the 1990 International Conference on Parallel Processing** Benjamin W. Wah,David A. Padua,Pen-Chung Yew,Pennsylvania State University,1990 The field of parallel processing is maturing into a strong diverse and highly international area of study For the 1990 conference the eighteenth to be held over 500 papers were submitted by authors from 28 countries These three volumes make available the 256 papers that were accepted after a rigorous process of review The overall variety and currency of issues addressed by the collection will make these volumes of vital interest to engineers scientists programmers systems analysts managers and other involved in the theory design and application of parallel processing   *The Transactions of the Institute of Electronics and Communication Engineers of Japan* Denshi Tsūshin Gakkai,1986

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