VLSI DESIGN METHODOLOGIES FOR DIGITAL SIGNAL PROCESSING ARCHITECTURES

edited by

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Vlsi Design Methodologies For Digital Signal Processing Architectures

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Vlsi Design Methodologies For Digital Signal Processing Architectures:

   VLSI Design Methodologies for Digital Signal Processing Architectures Magdy A. Bayoumi,2012-12-06 Designing VLSI systems represents a challenging task It is a transfonnation among different specifications corresponding to different levels of design abstraction behavioral stntctural and physical The behavioral level describes the functionality of the design It consists of two components static and dynamic The static component describes operations whereas the dynamic component describes sequencing and timing The structural level contains infonnation about components control and connectivity The physical level describes the constraints that should be imposed on the floor plan the placement of components and the geometry of the design Constraints of area speed and power are also applied at this level To implement such multilevel transfonnation a design methodology should be devised taking into consideration the constraints limitations and properties of each level The mapping process between any of these domains is non isomorphic A single behavioral component may be transfonned into more than one structural component Design methodologies are the most recent evolution in the design automation era which started off with the introduction and subsequent usage of module generation especially for regular structures such as PLA s and memories A design methodology should offer an integrated design system rather than a set of separate unrelated routines and tools A general outline of a desired integrated design system is as follows Decide on a certain unified framework for all design levels Derive a design method based on this framework Create a design environment to implement this design method   Vlsi Design Methodologies For Digital Signal Processing Architectures Bayoumi,2005-01-01   **VLSI Digital Signal Processing Systems** Keshab K. Parhi,1999 This graduate level textbook brings together the distinct fields of computer architecture theory and digital signal processing Covering a large amount of material in a single self contained volume it links VLSI architecture with the algorithms on which the accompanying hardware is based   **VLSI Digital Signal Processors** Vijay Madisetti,1995 This is the only book that offers a thorough treatment of the following design and application of programmable digital signal processors formal specification and optimization of signal processing architectures and circuits high level synthesis of DSP architectures and datapaths detailed treatment of application specific integrated circuits ASICs scheduling allocation and assignment algorithms for multiple processor DSP systems and hardware software co design issues in DSP VLSI Digital Signal Processors An Introduction to Rapid Prototyping and Design Synthesis provides a cohesive quantitative and clear exposition of the implementation and prototyping of digital signal processing algorithms on programmable signal processors parallel processing systems and application specific ICs Included are both programmable and dedicated digital signal processors and discussions of the latest optimization methods and the use of computer aided design techniques   **FPGA-based Implementation of Signal Processing Systems** Roger Woods,John McAllister,Gaye Lightbody,Ying Yi,2008-10-13 Field programmable gate arrays FPGAs are an increasingly popular technology for implementing digital signal processing DSP systems By allowing designers

to create circuit architectures developed for the specific applications high levels of performance can be achieved for many DSP applications providing considerable improvements over conventional microprocessor and dedicated DSP processor solutions The book addresses the key issue in this process specifically the methods and tools needed for the design optimization and implementation of DSP systems in programmable FPGA hardware It presents a review of the leading edge techniques in this field analyzing advanced DSP based design flows for both signal flow graph SFG based and dataflow based implementation system on chip SoC aspects and future trends and challenges for FPGAs The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail Written from a system level design perspective and with a DSP focus the authors present many practical application examples of complex DSP implementation involving high performance computing e g matrix operations such as matrix multiplication high speed filtering including finite impulse response FIR filters and wave digital filters WDFs adaptive filtering e g recursive least squares RLS filtering transforms such as the fast Fourier transform FFT FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest *Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology* ,1998 Design Methodologies Satoshi Gotō,1986 Recent advances in microelectronics technology have drastically changed the strategies used in VLSI circuits Establishing an efficient methodology is one of the keys to successful VLSI chip design This book is intended to provide new efficient design methodologies developed by outstanding researchers in this field These methodologies are described from theoretical and practical points of view with the aid of colour illustrations As well as being a useful introductory guide for researchers or students the book can serve as a useful reference source for designers involved in the development of VLSI systems VLSI design examples are shown as a reference for practicing engineers and all those interested in understanding and improving VLSI architecture for 32 bit microprocessors or signal processors **VLSI Digital Signal Processing** Magdy A. Bayoumi,1979* High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The

editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures Design Tools and Architectures for Dedicated Digital Signal Processing (DSP) Processors ,1996 The work reported in this document is concerned with the development of CAD tools design methodologies and architectures for the following topics of VLSI digital signal processing high level transformations and synthesis discrete wavelet transform high speed digital subscriber loops and finite field arithmetic for use in Reed Solomon coders Through this research we developed fast and efficient algorithms ILP models and tools that would reduce the time to explore the design space and locate an area optimal design of ASICs for DSP applications within a heterogeneous environment **VLSI Signal Processing Technology** Magdy Bayoumi,Earl Swartzlander,1994-10-31 This book is the first in a set of forthcoming books focussed on state of the art development in the VLSI Signal Processing area It is a response to the tremendous research activities taking place in that field These activities have been driven by two factors the dramatic increase in demand for high speed signal processing especially in consumer elec tronics and the evolving microelectronic technologies The available technology has always been one of the main factors in determining al gorithms architectures and design strategies to be followed With every new technology signal processing systems go through many changes in concepts design methods and implementation The goal of this book is to introduce the reader to the main features of VLSI Signal Processing and the ongoing developments in this area The focus of this book is on Current developments in Digital Signal Processing DSP pro cessors and architectures several examples and case studies of existing DSP chips are discussed in Chapter 1 Features and requirements of image and video signal processing architectures both applications specific integrated circuits ASICs and programmable image processors are studied in Chapter 2 New market areas for signal processing especially in consumer electronics such as multimedia teleconferencing and movie on demand Impact of arithmetic circuitry on the performance of DSP pro cessors several topics are discussed in Chapter 3 such as number representation arithmetic algorithms and circuits and implementa tion **VLSI Signal Processing, VIII** IEEE Signal Processing Society,1995 **MARS: a High-level Synthesis Tool for Digital Signal Processing Architecture Design** Ching-Yi Wang,1992 The Electrical Engineering Handbook Wai Kai Chen,2004-11-16 The Electrical Engineer s Handbook is an invaluable reference source for all practicing electrical engineers and students Encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students This text will most likely be the engineer s first choice in looking for a solution extensive complete references to other sources are provided throughout No other book has the breadth and depth of coverage available here This is a must have for all practitioners and students The Electrical Engineer s Handbook provides the most up to date information in Circuits and

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