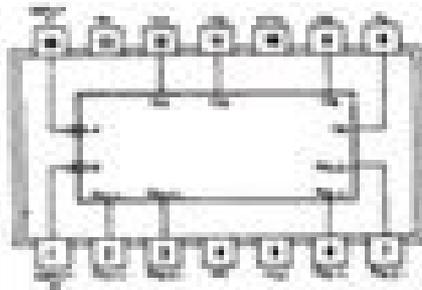
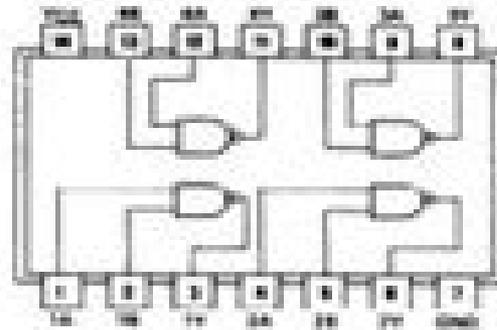


PIN CONFIGURATIONS OF THE INTEGRATED CIRCUIT CHIPS

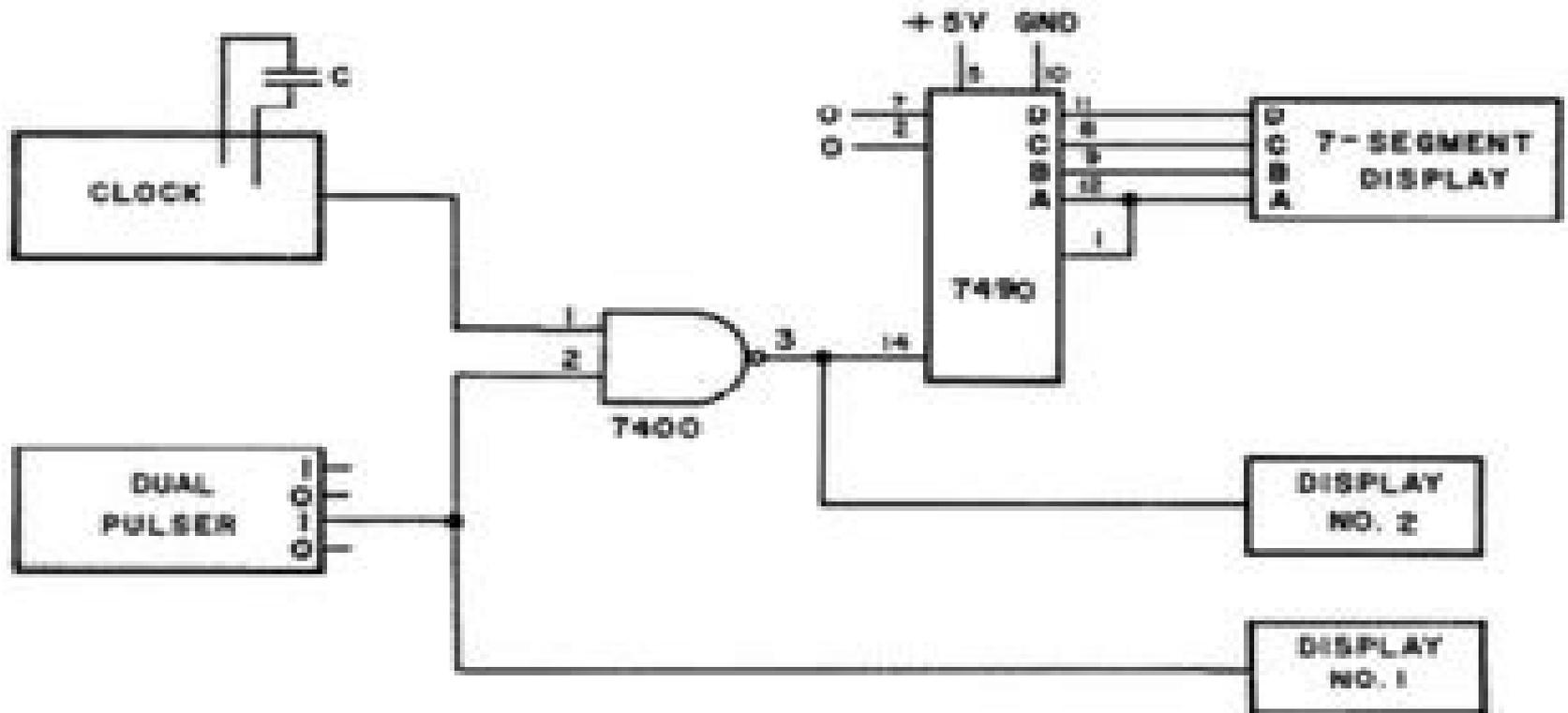


7490



7400

SCHEMATIC DIAGRAM OF CIRCUIT



Technibook I Logic Memory Experiments Using Ttl Integrated Circuits

David G. Larsen, Peter R. Rony



Technibook I Logic Memory Experiments Using Ttl Integrated Circuits:

Technibook I Logic Memory Experiments Using Ttl Integrated Circuits Book Review: Unveiling the Power of Words

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