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# **VLSI Specification, Verification and Synthesis**

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**Graham Birtwistle  
P.A. Subrahmanyam**

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# Vlsi Specification Verification And Synthesis

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## **Vlsi Specification Verification And Synthesis:**

**VLSI Specification, Verification and Synthesis** Graham Birtwistle, P.A. Subrahmanyam, 2012-12-06 VLSI Specification Verification and Synthesis Proceedings of a workshop held in Calgary from 12-16 January 1987 The collection of papers in this book represents some of the discussions and presentations at a workshop on hardware verification held in Calgary January 12-16 1987 The thrust of the workshop was to give the floor to a few leading researchers involved in the use of formal approaches to VLSI design and provide them ample time to develop not only their latest ideas but also the evolution of these ideas In contrast to simulation where the objective is to assist in detecting errors in system behavior in the case of some selected inputs the intent of hardware verification is to formally prove that a chip design meets a specification of its intended behavior for all acceptable inputs There are several important applications where formal verification of designs may be argued to be cost effective Examples include hardware components used in safety critical applications such as flight control industrial plants and medical life support systems such as pacemakers The problems are of such magnitude in certain defense applications that the UK Ministry of Defense feels it cannot rely on commercial chips and has embarked on a program of producing formally verified chips to its own specification Hospital civil aviation and transport boards in the UK will also use these chips A second application domain for verification is afforded by industry where specific chips may be used in high volume or be remotely placed

**Formal VLSI Specification and Synthesis** Luc J. M. Claesen, 1990 Functional and behavioral verification of correctness forms the bottleneck in current VLSI design systems For economical reasons design of VLSI circuits must be completely validated before manufacturing Current VLSI validation is mainly done through extensive simulation The emerging alternative is based on formal design and verification methods that guarantee correctness This book describes original work in all aspects of formal hardware design methods Topics covered include high level specification hardware description languages formal hardware verification methods guided synthesis methods correctness preserving transformations use of theorem provers for verification formal proof of correctness MOS timing verification methods design for verifiability and practical experiences

[Higher Order Logic Theorem Proving and Its Applications](#) Jeffrey J. Joyce, Carl-Johan H. Seger, 1994-04-28 This volume constitutes the refereed proceedings of the 1993 Higher Order Logic Users Group Workshop held at the University of British Columbia in August 1993 The workshop was sponsored by the Centre for Integrated Computer System Research It was the sixth in the series of annual international workshops dedicated to the topic of Higher Order Logic theorem proving its usage in the HOL system and its applications The volume contains 40 papers including an invited paper by David Parnas McMaster University Canada entitled Some theorems we should prove

**Theorem Provers in Circuit Design** Ramayya Kumar, Thomas Kropf, 1995-03-06 This two volume set contains papers presented at the International Conference on Computational Engineering Science ICES 95 held in Mauna Lani Hawaii from 30 July to 3 August 1995 The contributions capture the state of the science in computational

modeling and simulation in a variety of engineering disciplines civil mechanical aerospace materials and electronics engineering

**The Circuits and Filters Handbook** Wai-Kai Chen,2002-12-23 A bestseller in its first edition The Circuits and Filters Handbook has been thoroughly updated to provide the most current most comprehensive information available in both the classical and emerging fields of circuits and filters both analog and digital This edition contains 29 new chapters with significant additions in the areas of computer

**Designing Correct Circuits** Geraint Jones,Mary Sheeran,2013-12-14 These proceedings contain the papers presented at a workshop on Designing Correct Circuits jointly organised by the Universities of Oxford and Glasgow and held in Oxford on 26 28 September 1990 There is a growing interest in the application to hardware design of the techniques of software engineering As the complexity of hardware systems grows and as the cost both in money and time of making design errors becomes more apparent so there is an eagerness to build on the success of mathematical techniques in program development The harsher constraints on hardware designers mean both that there is a greater need for good abstractions and rigorous assurances of the trustworthiness of designs and also that there is greater reason to expect that these benefits can be realised The papers presented at this workshop consider the application of mathematics to hardware design at several different levels of abstraction At the lowest level of this spectrum Zhou and Hoare show how to describe and reason about synchronous switching circuits using UNiLY a formalism that was developed for reasoning about parallel programs Aagaard and Leeser use standard mathematical techniques to prove correct their implementation of an algorithm for Boolean simplification The circuits generated by their formal synthesis system are thus correct by construction Thuau and Pilaud show how the declarative language LUSTRE which was designed for programming real time systems can be used to specify synchronous circuits

**Correct Hardware Design and Verification Methods**,1993

**Formal Methods in Computer-Aided Design** Mandayam Srivas,Albert Camilleri,1996-10-23 This book constitutes the refereed proceedings of the First International Conference on Formal Methods in Computer Aided Design FMCAD 96 held in Palo Alto California USA in November 1996 The 25 revised full papers presented were selected from a total of 65 submissions also included are three invited survey papers and four tutorial contributions The volume covers all relevant formal aspects of work in computer aided systems design including verification synthesis and testing

Computer Hardware Description Languages and their Applications D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering

advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification

*The Fusion of Hardware Design and Verification* George J. Milne,1988

*VLSI 89* Gerald Musgrave,U. Lauther,1990 Continuing the tradition established during the last decade the latest VLSI volume breaks new ground this time with such topics as Mechatronics and Three Dimensional Chip Architectures The selected research papers presented here have each been refereed by six people Theoreticians in the field present their latest research while cost conscious practitioners demonstrate the reality of the field In other words the combination of breadth and depth should satisfy the most discerning readers and excite the younger generation ensuring continued growth of the field

*Formal Verification of Hardware Design* Michael Yoeli,1990

**The Formal Verification of the VIPER Microprocesor [i.e. Microprocessor]** Tejkumar Arora,1990

**Electrical & Electronics Abstracts** ,1997

**Hardware Specification, Verification and Synthesis** Miriam Leeser,Geoffrey Brown,1990

**Higher Order Logic Theorem Proving and Its Applications** ,1994

**Computer-aided Verification** ,1991

**Digital Systems Reference Book** Brian Holdsworth,Graham R. Martin,1993 Designed to provide comprehensive coverage of the field of digital systems in a concise but authoritative form For ease of access the book has been divided into five parts fundamentals devices for digital systems system design and techniques system development and applications

**Euromicro Symposium on Microprocessing and Microprogramming** ,1988

*Proceedings of the International Conference on Application Specific Array Processors, September 2-4, 1991, Barcelona, Spain* Mateo Valero,1991 The International Conference on Application Specific Array Processors ASAP 91 was held September 1991 in Barcelona Spain Application specific computing may be the solution to many computationally intensive problems Coverage here includes design mapping techniques synthesis and verification and systolic array designs and design methodologies No index Annotation copyrighted by Book News Inc Portland OR

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