# Special Computer Architectures for Pattern Processing

Edited by
King-Sun Fu, Tadao Ichikawa

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# Special Computer Architectures For Pattern Processing

**P.P. Jonker**

**Special Computer Architectures For Pattern Processing:**

**Special Computer Architectures for Pattern Processing** King-Sun Fu,2018-05-04 It has been recognized for a long time that a conventional sequential processor is inefficient for operations on pictorial data where relatively simple operations need to be performed on a large number of data elements pixels Though many parallel processing architectures for picture processing have been proposed in the past very few have actually been implemented due to the costs involved With LSI technology it is becoming possible to realize parallel architectures at a modest cost In the following the authors review some of the proposed architectures for pattern recognition and image processing *Special Computer Architectures for Pattern Processing* King-Sun Fu,2018-05-04 It has been recognized for a long time that a conventional sequential processor is inefficient for operations on pictorial data where relatively simple operations need to be performed on a large number of data elements pixels Though many parallel processing architectures for picture processing have been proposed in the past very few have actually been implemented due to the costs involved With LSI technology it is becoming possible to realize parallel architectures at a modest cost In the following the authors review some of the proposed architectures for pattern recognition and image processing **Computer Architectures for Spatially Distributed Data** Herbert Freeman,G.G. Pieroni,2013-11-09 These are the proceedings of a NATO Advanced Study Institute ASI held in Cetraro Italy during 6 17 June 1983 The title of the ASI was Computer Arehiteetures for SpatiaZZy vistributed Vata and it brouqht together some 60 participants from Europe and America Presented ere are 21 of the lectures that were delivered The articles cover a wide spectrum of topics related to computer architecture s specially oriented toward the fast processing of spatial data and represent an excellent review of the state of the art of this topic For more than 20 years now researchers in pattern recognition image processing meteorology remote sensing and computer engineering have been looking toward new forms of computer architectures to speed the processing of data from two and three dimensional processes The work can be said to have commenced with the landmark article by Steve Unger in 1958 and it received a strong forward push with the development of the ILIAC III and IV computers at the University of Illinois during the 1960 s One clear obstacle faced by the computer designers in those days was the limitation of the state of the art of hardware when the only switching devices available to them were discrete transistors As aresult parallel processing was generally considered to be imprae tieal and relatively little progress was made **VLSI for Pattern Recognition and Image Processing** K.S. Fu,2013-03-08 During the past two decades there has been a considerable growth in interest in problems of pattern recognition and image processing PRIP This inter est has created an increasing need for methods and techniques for the design of PRIP systems PRIP involves analysis classification and interpretation of data Practical applications of PRIP include character recognition re mote sensing analysis of medical signals and images fingerprint and face identification target recognition and speech understanding One difficulty in making PRIP systems practically feasible and hence more popularly used is the requirement

of computer time and storage This situation is particularly serious when the patterns to be analyzed are quite complex Thus it is of the utmost importance to investigate special comput er architectures and their implementations for PRIP Since the advent of VLSI technology it is possible to put thousands of components on one chip This reduces the cost of processors and increases the processing speed VLSI algorithms and their implementations have been recently developed for PRIP This book is intended to document the recent major progress in VLSI system design for PRIP applications **Morphological Image Processing: Architecture and VLSI design** P.P. Jonker,2012-12-06 Summary Based on the experiences of past designs and the outcome of recent studies in the comparisons of low level image processing architectures a pipelined system for real time low image processing has been designed and realized in CMOS technology To minimize design pitfalls a study was performed to the details of the design solutions that have been found in embodimentsof the three main architectural groups of image processing the Square Processor Arrays the Linear Processor Arrays and the Pipelines This is reflected in a theoretical model As the design is based on bitplane wise processing of images research was performed on the principles ofCellularLogic Processing of two dimensional images of binary A methodology has been developed that is based on the transformation images using sets of Hit or Miss masks This method appeared to be extendable to higher dimensional images A theoretical model for the generation of break point conditions in high dimensional images has been developed and applied up to dimension three Multicomputers and Image Processing Kendall Preston,1982-01-01 Multicomputers and Image Processing Algorithms and Programs is the second of a set presenting papers from the third meeting held in Madison Wisconsin on May 27 30 1981 The workshop explores the large and powerful multicomputer arrays and networks with particular emphasis on the related aspects of developing algorithms and programs for multicomputer architectures Separating 33 papers into chapters this book reflects the three major aspects of the problem user algorithms and programs higher level languages and multicomputer architectures The first chapters present specific larger structure as well as whole program algorithms and their respective applications Other chapters describe the important high level programming of Fortran based language for the massively parallel processors and the set theory based language for expressing the structural image processing and perceptual operations effected by Cytocomputer and other cellular array motivated architectures The concluding chapters examine several major types of computer architectures that are being developed for large parallel serial multicomputer systems This book is of great value to computer programmers and scientists Fundamentals in Computer Vision O. D. Faugeras,1983-07-28 *1985 IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management, Miami Beach, Florida, November 18-20, 1985* IEEE Computer Society,1985 VLSI Architectures for Image Processing and Pattern Recognition Charles Hokshun Tong,1986 Multi-Computer Architectures for Artificial Intelligence Leonard Uhr,1987-02-20 This volume is the most current and complete evaluation of multi computer networks It presents an up to date and detailed survey which describes the large number of multi computer architectures

that have been built during the past few years and the even greater variety that are presently being built designed and explored Special emphasis is given to approaches that show promise for handling complex real world problems in real time Describes the range of problems that confront workers in artificial intelligence AI and explains some of the major approaches being taken by AI researchers focusing on the robust flexible extremely fast hence highly parallel systems Presents judgements and opinions as to the most promising multi computer archtectures for AI including a number of exciting multi computer structures *1983 IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management* ,1983 **IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management** ,1985 **Proceedings of the … Scandinavian Conference on Image Analysis** ,1980 Algorithm-structured Computer Arrays and Networks Leonard Merrick Uhr,1984 Algorithm Structured Computer Arrays and Networks **Digital Image Signal Processing** Friedrich M. Wahl,1987 Computer Architecture, Tutorial Daniel D. Gajski,1987 This tutorial is intended for computer system architects designers and managers who need a broad range of knowledge on advanced topics in computer architecture The book can be used as a textbook or as a research and design reference The goal of this tutorial is to present the state of the art in advanced computer architecture Part I deals with the concepts underlying current architectures Part II covers a variety of approaches and techniques being used in the design of advanced computer systems Conference Proceedings ,1984 **Handbook of Computer Vision Algorithms in Image Algebra** Gerhard X. Ritter,Joseph N. Wilson,1996-05-02 A reference for programmers in image algebra This book is designed as an introduction to the basic concepts of image algebra enabling engineers and scientists of any level to gain an understanding of the topic **Optical Technology for Signal Processing Systems** Mark P. Bendett,1991 **Proceedings of the 1986 International Conference on Parallel Processing** ,1986

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