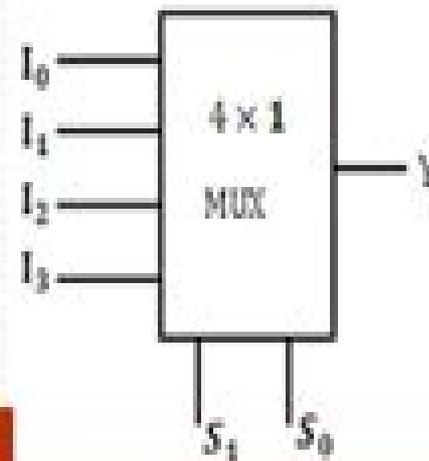
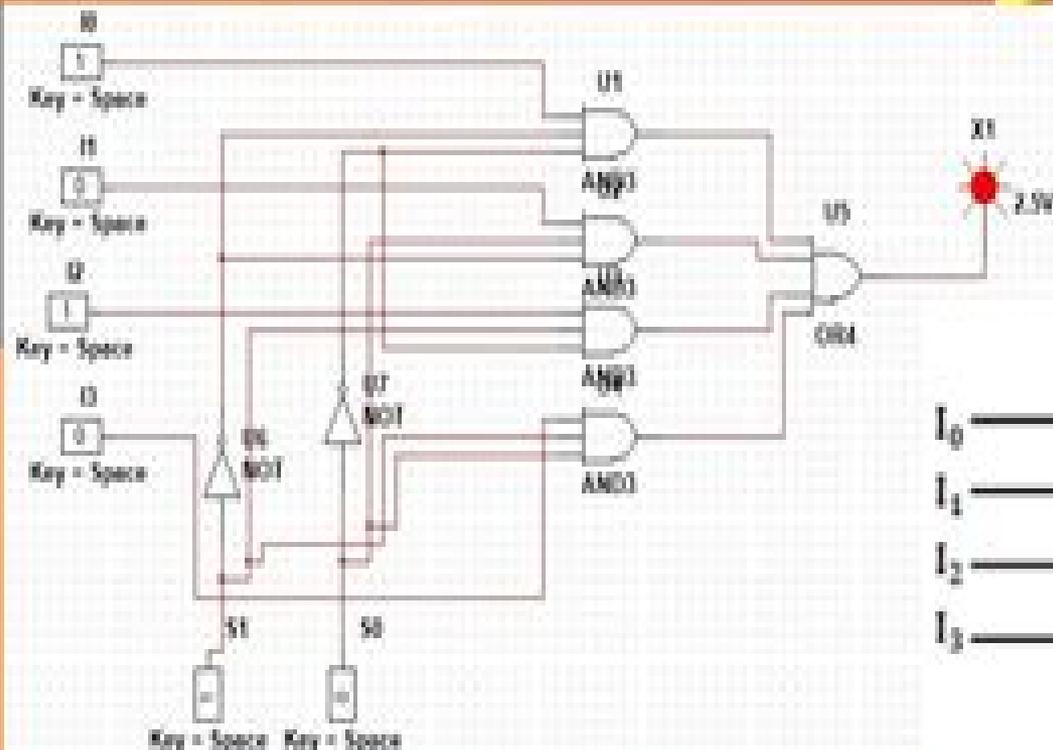


Design of a 4X1 Multiplexer Using Multisim



Multisim



Truth table

S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3

#10

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