



Microcontroller Based Assembly Check and Built-In Self Test

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ABSTRACT: In present large scale industries where production per day is of the orders of few hundreds to few thousands, it is difficult to check whether each component assembled on PCB (Printed Circuit Board) is in the proper position or not and also to check whether all the designed components are present on the board. If the manual method is used to perform this process, production cost increases and decreases the rate of production. In manual method an electronic engineer or technician who has knowledge of all electronic components will check component by component and will send the tested board to next section if everything is correct else it will be rejected which is time consuming. A solution to solve this problem is to replace the current system with a cost of around 2 to 3crore which is used to design an electronic system which tests all the sections of the board and will give clear report of all assembled components at cost of 20 to 25 thousand rupees (low cost systems), which will be helpful for small scale industries. If one or more components are not present, it will automatically detect and discard it as defective PCB and if all the designed components are present it will display the details of component checked and their position.

KEYWORDS: Printed Circuit Board (PCB), Assembly Check, Electronic components, MPLAB IDE, Embedded system.

INTRODUCTION

The novel idea of this project is to achieve higher production rate in a large scale industry by using this embedded system. This project is basically a microcontroller based design used to check the assembly of all the components in a PCB (Printed Circuit Board). When all the components are present then it continues to check the voltages in different sections of the board and gives decision about the working performance of each section of the circuit under test.

It consists of a platform on which the assembled board is placed in specific position. Once the system is switched on, it starts checking the components one by one and display the states of operation. When the specific component is not found in the specific position, it will display the name of components and their specifications on the display unit and rejects the board since it cannot be sent to next section for testing.

Three stepper motors are used in the project to check position of each component on the board. First motor move in X-direction and second will move in Y-direction and third motor in Z-direction is used to check the components. The speed, direction of movement and the displacement of all the motors depends on the values specified in the software. Since stepper motor requires high current for its operation Darlington amplifiers are used. The power supply unit consisting step down transformer, rectifier, filter and regulator IC (Integrated Circuit) is used to supply different DC voltages to different sections of the circuit.

The MPLAB IDE (Microprocessor Lab Integrated Development Environment) is used in this project, a window based platform for designing the software. The program is simulated in the system using simulator and then is tested with the hardware modules.

II. LITERATURE SURVEY

Printed circuit boards (PCBs) testing is becoming more expensive and difficult due to the complexity of PCBs design. The common methods for diagnosing PCBs still suffering from many difficulties; it needs long time, a lot of manual

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Transputers '94 Monique Becker, Luc Litzler, Michel Trehel, 1994-01-01 **Intelligent Image Processing, Data Analysis & Information Retrieval** Masoud Mohammadian, 1999 This edited volume is dedicated to the theory and applications of Computational Intelligence techniques for Intelligent Image Processing Data Analysis and Information Retrieval It consists of 52 accepted research papers from the 1999 International Conference on Computational Intelligence for Modeling Control and Automation CIMCA 99 The goal of this conference was to provide a medium for the exchange of ideas between theoreticians and practitioners to address the important issues in computational intelligence for modelling control and automation The research papers presented in this book cover new techniques and applications in the of Image Processing Computer Vision Multimedia Systems Filtering Classification Data Analysis Prediction Intelligent Database and Information Retrievals **Correct Models of Parallel Computing** S. Noguchi, Ota Masahiro, 1997 The 21st century will be the age of network computing Among the many key technologies in this field parallel computing and networking technology will play very important roles In this book emphasis is placed on networking and modeling parallel computing The topics cover parallel computing algorithms parallel software massively parallel computing systems and related applications Articles cover parallel computing networking and related applications to initiate discussions Since the appearance of Transputer chip T9000 C104 and standardizations of IEEE1355 Transputer systems seem to have opened a new interesting area of parallel computing networking and many practical applications Architectures, Languages and Techniques for Concurrent Systems World Occam and Transputer User Group. Technical Meeting, 1999 During the past fifteen years concurrency in programming languages such as Java rose and fell and again became popular At this moment developers advise us to avoid concurrency in programming They are using a host of deprecated methods in the latest releases How are we to understand the love hate relationship with what should be a widely used approach of tackling real world problems The aim of rchitectures Languages and Techniques is to encourage the safe efficient and effective use of parallel computing It is generally agreed that concurrency is found in most real applications and that it should be natural to use concurrency in programming However there has grown up a myth that concurrency is hard and only for the hardened expert The papers collected in this book cover the whole spectrum of concurrency from theoretical underpinnings to applications The message passing style of concurrency developed in the Communicating Sequential Processes CSP approach is considered and extensions are proposed CSP s realization in the programming language occam is used directly for applications as diverse as modeling of concurrent systems and the description of concurrent hardware This latter application may be compared to the use of Java for the same purpose Concurrency and the use of Java is the subject of further papers as is the provision of CSP like facilities in Java and C and techniques to use these languages to construct reliable concurrent systems At a time when concurrency gives headaches this book brings a welcome breath of fresh air Concurrency can really be a positive way

forward **Communicating Process Architectures 2000** World Occam and Transputer User Group. Technical Meeting, 2000 Modern computing systems are built in terms of components and those components communicating Communication systems imply concurrency which is a theme of the WoTUG series Traditionally concurrency has been taught considered and experienced as an advanced and difficult topic The thesis underlying this conference is that that idea is wrong The natural world operates through continuous interaction of massive numbers of autonomous agents at all levels sub atomic human astronomic It seems it is time to mature concurrency into a core engineering discipline that can be used on an everyday basis to simplify problem solutions as well as to enable them The goal of Communicating Process Architectures 2000 was to stimulate discussion and ideas as to the role concurrency should play in future generations of scalable computer infrastructure and applications where scaling means the ability to ramp up functionality stay in control as complexity increases as well as physical metrics such as performance *Communicating Process Architectures 2001* World Occam and Transputer User Group. Technical Meeting, World Occam and Transputer User Group, Technical Meeting, 2001 Concurrency is an integral part of everyday life The concept is so ingrained in our existence that we benefit from it without realizing When faced with a taxing problem we automatically involve others to solve it more easily Such concurrent solutions to a complex problem may however not be quite straightforward and communication becomes crucial to ensure the successful solution of the problem **Advances in Computer and Information Sciences '98** U. Gudukbay, 1998 This volume includes very high quality papers in different areas of computer and information sciences The main themes are computer network performance evaluation and artificial neural networks and their applications The latest developments in these areas are presented by a number of distinguished researchers from all over the world These proceedings of The 13th International Symposium on Computer and Information Sciences ISCIS 98 contain outstanding papers specifically related to the areas of Gelenbe neural networks and their applications performance of computer communication networks simulations and analytic methods in order to study the performance of telecommunication networks scheduling and resource allocation in computer and multimedia systems stochastic ordering applied to performance evaluation and simulation of virtual humans

Computational Intelligence for Modelling, Control & Automation Masoud Mohammadian, 1999 This edited Book is dedicated to the theory and applications of Evolutionary Computation and Fuzzy Logic for Intelligent Control Knowledge Acquisition and Information Retrieval The book consists of 86 selected research papers from the 1999 International Conference on Computational Intelligence for Modelling Control and Automation CIMCA 99 The research papers presented in this book cover new techniques and applications in the following research areas Evolutionary Computation Fuzzy Logic and Expert Systems with their applications for Optimisation Learning Control Scheduling and Multi Criteria Analysis as well as Reliability Assessment Information Retrieval and Knowledge Acquisition **Communicating Process Architectures 2002** World Occam and Transputer User Group. Technical Meeting, 2002 The WoTUG series of conferences are a major

forum for the presentation of state of the art ideas on concurrency and communication This book continues this trend with these proceedings containing a number of papers that discuss a wide range of issues fundamental to the future of concurrency

The Network Designer's Handbook A. M. Jones,1997 The Network Designer s Handbook will help anyone trapped between limited bandwidth fault intolerant computer buses and expensive over engineered telecommunications technology It will help anyone looking for new cost effective ways to build LAN switches RAID systems multimedia servers or multiprocessors It will help the small company looking for an edge to break into the market and the large one looking for ways to improve its margins and boost its market share This handbook the result of over six man years of effort at the PACT Research Institute provides solid engineering data for the computer systems professional Four cpu months of system simulation are summarised in an easy to read form allowing the consequences of different design decisions to be simply compared The Network Designer s Handbook explains the principles of the new generation of small scale low buffer serial interconnects Using the specific example of IEEE 1355 1995 links and the STC104 high valency switch chip it shows how this technology can provide modular fault tolerant and scalable interconnect The picture is rounded out with descriptions of network topologies case studies and many practical tips

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Proceedings ,1997 *Computer Systems Science & Engineering* ,1994 Parallel Programming and Java World Occam and Transputer User Group. Technical Meeting,1997 *Robot Control 1994 (SYROCO '94)* Lorenzo Sciavicco,Claudio Bonivento,F. Nicolò,1995 *Proceedings of the Sixth Euromicro Workshop on Parallel and Distributed Processing* Euromicro Workshop on Parallel and Distributed Processing,1998 This volume covers issues in parallel and distributed processing Coverage includes communications application caching scheduling distributed systems design and verification and real time data organization

Transputers '92 Monique Becker,Luc Litzler,Michel Trehel,1992 In today s highly competitive environment the transputer market provides Europe with a great number of important assets From the first transputer with its four links and OCCAM language which opened the door to a whole series of distributed memory machines to the T9000 with the C104 and the standardization of software programs progress in this field has come a long way

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