

System Level Hardware/ Software Co-design

An Industrial Approach

Joris van den Hurk and Jochen Jess

Springer-Science+Business Media, B.V.

System Level Hardware Software Co Design An Industrial Approach

Chao Zhang



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System Level Hardware/Software Co-Design Joris van den Hurk, Jochen A.G. Jess, 2013-04-17 Hierarchical design methods were originally introduced for the design of digital ICs and they appeared to provide for significant advances in design productivity Time to Market and first time right design These concepts have gained increasing importance in the semiconductor industry in recent years In the course of time the supportive quality of hierarchical methods and their advantages were confirmed System Level Hardware Software Co design An Industrial Approach demonstrates the applicability of hierarchical methods to hardware software codesign and mixed analogue digital design following a similar approach Hierarchical design methods provide for high levels of design support both in a qualitative and a quantitative sense In the qualitative sense the presented methods support all phases in the product life cycle of electronic products ranging from requirements analysis to application support Hierarchical methods furthermore allow for efficient digital hardware design hardware software codesign and mixed analogue digital design on the basis of commercially available formalisms and design tools In the quantitative sense hierarchical methods have prompted a substantial increase in design productivity System Level Hardware Software Co design An Industrial Approach reports on a six year study during which time the number of square millimeters of normalized complexity an individual designer contributed every week rose by more than a factor of five Hierarchical methods therefore enabled designers to keep track of the ever increasing design complexity while effectively reducing the number of design iterations in the form of redesigns System Level Hardware Software Co design An Industrial Approach is the first book to provide a comprehensive coherent system design methodology that has been proven to increase productivity in industrial practice The book will be of interest to all managers designers and researchers working in the semiconductor industry

Hardware/Software Co-Design Giovanni DeMicheli, M.G. Sami, 2013-11-11 Concurrent design or co design of hardware and software is extremely important for meeting design goals such as high performance that are the key to commercial competitiveness Hardware Software Co Design covers many aspects of the subject including methods and examples for designing 1 general purpose and embedded computing systems based on instruction set processors 2 telecommunication systems using general purpose digital signal processors as well as application specific instruction set processors 3 embedded control systems and applications to automotive electronics The book also surveys the areas of emulation and prototyping systems with field programmable gate array technologies hardware software synthesis and verification and industrial design trends Most contributions emphasize the design methodology the requirements and state of the art of computer aided co design tools together with current design examples

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

Hardware/Software Co-Design Jørgen Staunstrup, Wayne Wolf, 2013-04-17 Introduction to Hardware Software Co Design presents a number of

issues of fundamental importance for the design of integrated hardware software products such as embedded communication and multimedia systems This book is a comprehensive introduction to the fundamentals of hardware software co design Co design is still a new field but one which has substantially matured over the past few years This book written by leading international experts covers all the major topics including fundamental issues in co design hardware software co synthesis algorithms prototyping and emulation target architectures compiler techniques specification and verification system level specification Special chapters describe in detail several leading edge co design systems including Cosyma LYCOS and Cosmos Introduction to Hardware Software Co Design contains sufficient material for use by teachers and students in an advanced course of hardware software co design It also contains extensive explanation of the fundamental concepts of the subject and the necessary background to bring practitioners up to date on this increasingly important topic *The Codesign of Embedded Systems: A Unified Hardware/Software Representation* Sanjaya Kumar, James H. Aylor, Barry W. Johnson, Wm.A. Wulf, 1995-11-30 Current practice dictates the separation of the hardware and software development paths early in the design cycle These paths remain independent with very little interaction occurring between them until system integration In particular hardware is often specified without fully appreciating the computational requirements of the software Also software development does not influence hardware development and does not track changes made during the hardware design phase Thus the ability to explore hardware software tradeoffs is restricted such as the movement of functionality from the software domain to the hardware domain and vice versa or the modification of the hardware software interface As a result problems that are encountered during system integration may require modification of the software and or hardware resulting in potentially significant cost increases and schedule overruns To address the problems described above a cooperative design approach one that utilizes a unified view of hardware and software is described This approach is called hardware software codesign The Codesign of Embedded Systems develops several fundamental hardware software codesign concepts and a methodology that supports them A unified representation referred to as a decomposition graph is presented which can be used to describe hardware or software using either functional abstractions or data abstractions Using a unified representation based on functional abstractions an abstract hardware software model has been implemented in a common simulation environment called ADEPT Advanced Design Environment Prototyping Tool This model permits early hardware software evaluation and tradeoff exploration Techniques have been developed which support the identification of software bottlenecks and the evaluation of design alternatives with respect to multiple metrics The application of the model is demonstrated on several examples A unified representation based on data abstractions is also explored This work leads to investigations regarding the application of object oriented techniques to hardware design The Codesign of Embedded Systems A Unified Hardware Software Representation describes a novel approach to a topic of immense importance to CAD researchers and designers alike Hardware-Software Co-Design of Embedded Systems Felice Balarin, Paolo Giusto, Attila

Jurecska, Claudio Passerone, Ellen Sentovich, Bassam Tabbara, M. Chiodo, Harry Hsieh, Luciano Lavagno, Alberto Sangiovanni-Vincentelli, Kei Suzuki, 2013-01-28 Embedded systems are informally defined as a collection of programmable parts surrounded by ASICs and other standard components that interact continuously with an environment through sensors and actuators The programmable parts include micro controllers and Digital Signal Processors DSPs Embedded systems are often used in life critical situations where reliability and safety are more important criteria than performance Today embedded systems are designed with an ad hoc approach that is heavily based on earlier experience with similar products and on manual design Use of higher level languages such as C helps structure the design somewhat but with increasing complexity it is not sufficient Formal verification and automatic synthesis of implementations are the surest ways to guarantee safety Thus the POLIS system which is a co design environment for embedded systems is based on a formal model of computation POLIS was initiated in 1988 as a research project at the University of California at Berkeley and over the years grew into a full design methodology with a software system supporting it Hardware Software Co Design of Embedded Systems The POLIS Approach is intended to give a complete overview of the POLIS system including its formal and algorithmic aspects Hardware Software Co Design of Embedded Systems The POLIS Approach will be of interest to embedded system designers automotive electronics consumer electronics and telecommunications micro controller designers CAD developers and students *Hardware/Software Co-Design and Co-Verification* Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2013-03-09 Co Design is the set of emerging techniques which allows for the simultaneous design of Hardware and Software In many cases where the application is very demanding in terms of various performances time surface power consumption trade offs between dedicated hardware and dedicated software are becoming increasingly difficult to decide upon in the early stages of a design Verification techniques such as simulation or proof techniques that have proven necessary in the hardware design must be dramatically adapted to the simultaneous verification of Software and Hardware Describing the latest tools available for both Co Design and Co Verification of systems Hardware Software Co Design and Co Verification offers a complete look at this evolving set of procedures for CAD environments The book considers all trade offs that have to be made when co designing a system Several models are presented for determining the optimum solution to any co design problem including partitioning architecture synthesis and code generation When deciding on trade offs one of the main factors to be considered is the flow of communication especially to and from the outside world This involves the modeling of communication protocols An approach to the synthesis of interface circuits in the context of co design is presented Other chapters present a co design oriented flexible component data base and retrieval methods a case study of an ethernet bridge designed using LOTOS and co design methodologies and finally a programmable user interface based on monitors Hardware Software Co Design and Co Verification will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design **A Practical**

Introduction to Hardware/Software Codesign Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction to the subject of embedded systems design with emphasis on integration of custom hardware components with software. The key problem addressed in the book is the following: how can an embedded systems designer strike a balance between flexibility and efficiency? The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem. The book covers four topics in hardware software codesign fundamentals: the design space of custom architectures, the hardware software interface, and application examples. The book comes with an associated design environment that helps the reader to perform experiments in hardware software codesign. Each chapter also includes exercises and further reading suggestions. Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera which will make the material in this book applicable to a greater number of courses where these tools are already in use. More examples and exercises have been added throughout the book. If I were teaching a course on this subject I would use this as a resource and text. If I were a student who wanted to learn codesign I would look for a course that at least used a similar approach. If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective I would read this book first before any other. When I first started learning about codesign as a practitioner a book like this would have been the perfect introduction. Grant Martin Tensilica

Hardware/software Codesign Joris van den Hurk, 1996 **Proceedings of the Third International Workshop on Hardware/Software Codesign**, 1994 The proceedings of the September 1994 workshop comprise 28 technical papers that represent several important trends in co design research: use of design case studies to drive research algorithms for hardware software partitioning, algorithms for system verification and validation, and a continuing interest in design representations. No index. Annotation copyright by Book News Inc Portland OR **IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis**, 2003 First IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis, 2003 *A Software Approach for Hardware/software Co-design of Reliable Embedded Systems* Antonio Rosario Miele, 2006 **FME '97 Industrial Applications and Strengthened Foundations of Formal Methods** John Fitzgerald, Cliff B. Jones, Peter Lucas, 1997-09-03 This book constitutes the refereed proceedings of FME 97, the 4th International Symposium of Formal Methods Europe devoted to Industrial Applications and Strengthened Foundations of Formal Methods held in Graz Austria in September 1997. The 35 revised full papers presented in the volume were selected from a total of 94 submissions. Formal methods and mathematically based techniques are increasingly recognized as a viable technology for the development and engineering of computing systems. The majority of the papers in this volume describe industrial applications, extensions to existing techniques, or case studies. Papers on theoretical aspects show clear potential applicability. **1997 IEEE International Conference on Acoustics, Speech, and Signal Processing**, 1997 14th Symposium on Integrated Circuits and Systems Design

Sociedade Brasileira de Computação, 2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BDD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc

1994 IEEE International Symposium on Circuits and Systems, 1994 Fourth International Workshop on Hardware/Software Co-Design, Codes/CASHE '96 Donald E. Thomas, Rolf Ernst, 1996 Embedded architecture co synthesis and system integration B Lin S Vercauteren and H De Man A multi level transformation approach to HW SW codesign a case study T K Y Cheung G Hellestrand and P Kanthamanon Fully parallel hardware software codesign for multi dimensional DSP applications M Sheliga N L Passos and E H M Sha A co design methodology based on formal specification and high level estimation C Carreras and others Speed up estimation for HW SW systems W Hardt and W Rosenstiel A framework for interactive analysis of timing constraints in embedded systems R K Gupta The interplay of run time estimation and granularity in HW SW partitioning J Henkel and R Ernst Partitioning and exploration strategies in the TOSCA co design flow A Balboni W Fornaciari and D Sciuto Process partitioning for distributed embedded systems J Hou and W Wolf Two level partitioning of image processing algorithms for the parallel map oriented machine R W Hartenstein J Becker and R Kress PACE a dynamic programming algorithm for hardware software partitioning P V Knudsen and J Madsen A model for the coanalysis of hardware and software architectures F Rose and others A case study in co design of communication controllers R Gerndt Formal verification of embedded systems based on CFMS networks F Balarin and others Towards a model for hardware and software functional partitioning F Vahid and T dm Le Implications of codesign as a natural constituent of a systems engineering discipline for computer based systems M Voss and O Hammerschmidt Uninterpreted co simulation for performance evaluation of HW SW systems J P Calvez D Heller and O Pasquier Fast and accurate hardware software co simulation using software timing estimates C Passerone and others

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology, 1999 **MEMS, NANO and Smart Systems** Li Yuan, 2011-11-29 Selected peer reviewed papers from the 2011 7th International Conference on MEMS NANO and Smart Systems ICMENS 2011 November 4 6 2011 Kuala Lumpur Malaysia

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