

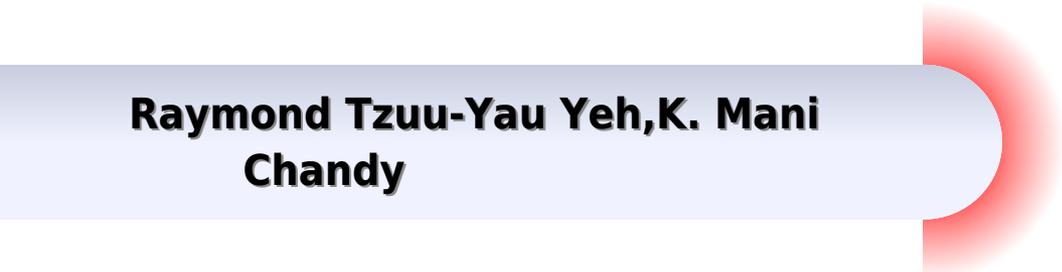


A specification language must:

- Be formal
- Founded on a mathematical theory (semantics)
- Allow proofs of properties
- Enable the development of support tools

Specification Language And Methodolgy

**Raymond Tzoo-Yau Yeh, K. Mani
Chandy**



Specification Language And Methodology:

SPECC: Specification Language and Methodology Daniel D. Gajski, Jianwen Zhu, Rainer Dömer, Andreas Gerstlauer, Shuqing Zhao, 2012-12-06 For the near future the recent predictions and roadmaps of silicon semiconductor technology all agree that the number of transistors on a chip will keep growing exponentially according to Moore's Law pushing technology towards the system on a chip SOC era However we are increasingly experiencing a productivity gap where the chip complexity that can be handled by current design teams falls short of the possibilities offered by technological advances Together with growing time to market pressures this drives the need for innovative measures to increase design productivity by orders of magnitude It is commonly agreed that the solutions for achieving such a leap in design productivity lie in a shift of the focus of the design process to higher levels of abstraction on the one hand and in the massive reuse of predesigned complex system components intellectual property IP on the other hand In order to be successful both concepts eventually require the adoption of new languages and methodologies for system design backed up by the availability of a corresponding set of system level design automation tools This book presents the SpecC system level design language SLDL and the corresponding SpecC design methodology The SpecC language is intended for specification and design of SOCs or embedded systems including software and hardware whether using fixed platforms integrating systems from different IPs or synthesizing the system blocks from programming or hardware description languages SpecC Specification Language and Methodology describes the SpecC methodology that leads designers from an executable specification to an RTL implementation through a well defined sequence of steps Each model is described and guidelines are given for generating these models from executable specifications Finally the SpecC methodology is demonstrated on an industrial size example The design community is now entering the system level of abstraction era and SpecC is the enabling element to achieve a paradigm shift in design culture needed for system product design and manufacturing SpecC Specification Language and Methodology will be of interest to researchers designers and managers dealing with system level design design flows and methodologies as well as students learning system specification modeling and design *Formal Methods and Software Engineering* Chris George, Huaikou Miao, 2003-06-30 This book constitutes the refereed proceedings of the 4th International Conference on Formal Engineering methods ICFEM 2002 held in Shanghai China in October 2002 The 43 revised full papers and 16 revised short papers presented together with 5 invited contributions were carefully reviewed and selected from a total of 108 submissions The papers are organized in topical sections on component engineering and software architecture method integration specification techniques and languages tools and environments refinement applications validation and verification UML and semantics *Advances in Design and Specification Languages for SoCs* Pierre Boulet, 2006-06-30 The seventh book in the CHDL Series is composed of a selection of the best articles from the Forum on Specification and Design Languages FDL 04 FDL is the European Forum to learn and exchange on new trends on the application of languages and

models for the design of electronic and heterogeneous systems The forum was structured around four workshops that are all represented in the book by outstanding articles Analog and Mixed Signal Systems UML based System Specification and Design C C Based System Design and Languages for Formal Specification and Verification The Analog and Mixed Signal Systems contributions bring some answers to the difficult problem of co simulating discrete and continuous models of computation The UML based System Specification and Design chapters bring insight into how to use the Model Driven Engineering to design Systems on Chip The C C Based System Design articles mainly explore system level design with SystemC The Languages for Formal Specification and Verification is represented by an invited contribution on the use of temporal assertions for symbolic model checking and simulation And finally chapter in this book contributed by preeminent members of the automotive design industry presents the recent industry standard AutoSAR Overall Advances in Design and Specification Languages for SoCs is an excellent opportunity to catch up with the latest research developments in the field of languages for electronic and heterogeneous system design

LOTOSphere: Software Development with LOTOS Tommaso Bolognesi, Jeroen van de Lagemaat, Chris Vissers, 2012-12-06 LOTOS Language Of Temporal Ordering Specification became an international standard in 1989 although application of preliminary versions of the language to communication services and protocols of the ISO OSI family dates back to 1984 This history of the use of LOTOS made it apparent that more advantages than the pure production of standard reference documents were to be expected from the use of such formal description techniques LOTOSphere Software Development with LOTOS describes in depth a five year project that moved LOTOS out of the ISO tower into software engineering practice LOTOS became a vehicle for efficient yet formally based industrial software specification design verification implementation and testing LOTOSphere Software Development with LOTOS is divided into six parts The first introduces the reader to LOTOS and the project LOTOSphere The five remaining each treat an important part of the software development life cycle using LOTOS This is the first book to give a comprehensive treatment of the use of these formal description techniques in a software engineering environment It will thus be a valuable reference for researchers and software developers and can also be used as a text for an advanced course on the subject

Current Trends in Programming Methodology: Software specification and design Raymond Tzoo-Yau Yeh, K. Mani Chandy, 1977 V 1 Software specification and design v 3 Software modelling Ed K M Chandy and R T Yeh

Encyclopedia of Information Science and Technology, Third Edition Khosrow-Pour, D.B.A., Mehdi, 2014-07-31 This 10 volume compilation of authoritative research based articles contributed by thousands of researchers and experts from all over the world emphasized modern issues and the presentation of potential opportunities prospective solutions and future directions in the field of information science and technology Provided by publisher

Advanced Lectures on Software Engineering Peter Müller, 2010-05-09 Software defects lead to enormous costs for the software industry and society as a whole While testing is useful to find bugs it is insufficient to show the absence of certain kinds of errors or that a program

satisfies its specification Such high levels of software quality can be achieved by software verification that is by proving the correctness of a program with respect to its specification Software verification has seen tremendous progress during the last decade it continues to be an active research topic and is now also becoming increasingly popular among practitioners This tutorial contains selected papers from the LASER summer Schools 2007 and 2008 both of which focused on correctness Applied Software Verification in 2007 and Concurrency and Correctness in 2008 Topics covered include verification of fine grain concurrency and transactions the SCOOP model for concurrent object oriented programming the Spec programming and verification system verification in the prototype verification system PVS and multi core chip design *A New Interface Specification Methodology and Its Application to Transducer Synthesis* Gaetano Borriello, University of California, Berkeley. Computer Science Division, 1927 Interface specification has a wide range of uses during the design and evaluation of a circuit Waves digrams and the constraints they capture form the basis for an entire new set of CAD tools that reason about interface design synthesis evaluation and testing One of these applications the auto matic synthesis of interface transducers is highlighted in this dissertation **ACM Transactions on Software Engineering and Methodology** ,2005 *Algebraic Methodology and Software Technology* ,2002 *Architectures, Languages, and Algorithms* ,1989 **Proceedings, 1993 ACM Computer Science Conference** Stan C. Kwasny, John F. Buck, 1993 Reliable Software Technologies - Ada-Europe '99 Michael Gonzalez Harbour, Juan A. de la Puente, 2003-07-31 The Fourth International Conference on Reliable Software Technologies Ada Europe 99 took place in Santander Spain from June 7 to 11 1999 It was sponsored by Ada Europe the European federation of national Ada societies in cooperation with ACM SIGAda and Ada Spain and it was organized by members of the University of Cantabria and the Technical University of Madrid in Spain This was the 19th consecutive year of Ada Europe conferences which have always been the main Ada events in Europe with their counterparts being the ACM SIGAda conferences in the USA formerly Tri Ada The conference is not just devoted to the Ada language but rather to the more general area of reliable software technologies In this sense there are papers on formal methods testing software architectures and design software engineering tools etc We believe that the role of reliable software technologies is becoming increasingly important as computer applications control more and more of our everyday systems The goal of our conference is to contribute to advancing the state of the art of all the technologies that help us in achieving better and more reliable software at a lower overall cost Formal Methods and Digital Systems Validation for Airborne Systems John Rushby, 1993 **Formal Methods and Software Development. Proceedings of the International Joint Conference on Theory and Practice of Software Development (TAPSOFT), Berlin, March 25-29, 1985** Hartmut Ehrig, Christiane Floyd, Maurice Nivat, James Thatcher, 1985-03-01 **Requirements Engineering Environments** Yutaka Ōno, 1982 **Software Engineering, The Development Process** Richard H. Thayer, Mark J. Christensen, 2005-11-11 This book integrates a useful set of software engineering standards with accompanying papers on specific knowledge areas in software

engineering Used as a CSDP resource guide it should improve a test taker s ability to pass the IEEE CSDP Certified Software Development Professionals exam

A Formalisation of Design Methods Loe M. G. Feijs,1993 **Proceedings** ,1978

Synthesis, Construction, and Reduction Techniques for Modeling and Analyzing Concurrent Systems Yigang Chen,1992

Specification Language And Methodolgy Book Review: Unveiling the Magic of Language

In a digital era where connections and knowledge reign supreme, the enchanting power of language has become more apparent than ever. Its ability to stir emotions, provoke thought, and instigate transformation is truly remarkable. This extraordinary book, aptly titled "**Specification Language And Methodolgy**," published by a very acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound impact on our existence. Throughout this critique, we shall delve to the book is central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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