Computer Architecture: Advanced Topics v. 2: A Modern Synthesis

Dasgupta, Subrata

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Subrata Dasgupta

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Subject Guide to Books in Print ,1997 **Computer Architecture, Advanced Topics** Subrata Dasgupta,1988-09-29 Paperbound Books in Print ,1992 Computer Architecture Subrata Dasgupta,1988 Computer Architecture: Advanced topics. Part One, Introduction And Background. The Scope Of Computer Architecture ; Classification And Taxonomy Subrata Dasgupta,1989 **Books In Print 2004-2005** Ed Bowker Staff,Staff Bowker, Ed,2004 Books Out-of-print ,1986 Computer Architecture: Foundations Subrata Dasgupta,1989 **ADVANCED COMPUTER ARCHITECTURE.** KA. PARTHASARATHY,2011 *Handbook of Computer Architecture* Anupam Chattopadhyay,2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications **Next-Gen Computer Architecture** Smruti R Sarangi,2023-09-21 In the last decade we have seen pathbreaking advances in processor design and specialized hardware for graphics applications and neural networks This second level textbook on computer architecture captures the latest developments in the field It starts from basic undergraduate level computer architecture concepts and takes the reader through an exciting journey of 14 chapters The book is divided into three parts out of order processor design and GPUs multiprocessors and memory systems and advanced topics It specifically covers many state of the art topics such as the design of high performance GPUs and their graphics pipelines architectures of the latest Intel AMD and Qualcomm processors methods to design secure chips architectural simulation nonvolatile memory technologies and contains a thorough treatment of hardware for all kinds of ML algorithms It explains rigorous theoretical concepts in a very conversational style About the Author Prof Smruti Ranjan Sarangi is the Usha Hasteer Chair Professor in the Computer Science and Engineering Department at IIT Delhi joint appointment with the Department of Electrical Engineering He has published roughly 120 papers in the areas of computer architecture and systems software in prestigious conferences and journals has been granted 5 US patents and has filed 7 Indian patents He takes an avid interest in computer science teaching and has been very active in designing school level CS curricula for one of the largest school boards in the world and policies and curricula for undergraduate and postgraduate programs throughout India He has authored a textbook on undergraduate level computer architecture Basic Computer Architecture For many of his contributions he received the ACM Outstanding Contributions to Computing Education Award OCCE in 2022 Fundamentals of Modern Computer Architecture Sam Steed,2023-06-06 Fundamentals of Modern Computer Architecture From Logic Gates to Parallel Processing is a

comprehensive and accessible guide that takes you on a fascinating journey through the inner workings of computer systems From the fundamental building blocks of logic gates to the advanced concepts of parallel processing this book provides a solid foundation in modern computer architecture Written by experts in the field this book offers a clear and concise introduction to the key principles and techniques that shape the design and functionality of today s computer systems Each chapter explores important topics such as digital logic instruction set architecture memory hierarchies pipelining and parallel processing providing a deep understanding of how these components work together to execute complex tasks Key Features 1 Logical Progression Follow a logical progression from the basic principles of digital logic to advanced topics such as parallel processing ensuring a comprehensive understanding of computer architecture 2 Practical Examples Reinforce your learning with real world examples and case studies that demonstrate the practical application of computer architecture concepts in modern computing systems 3 Hands On Approach Engage in hands on exercises and projects that allow you to apply the concepts learned and gain practical experience in designing and optimizing computer architectures 4 Emerging Technologies Explore emerging trends and technologies including multicore processors graphics processing units GPUs and heterogeneous computing and understand their impact on modern computer architecture 5 Design Considerations Gain insights into the design considerations and trade offs involved in building efficient and scalable computer systems taking into account factors such as performance power consumption and reliability 6 Comprehensive Resources Access additional resources including online materials and references to further deepen your understanding and explore advanced topics in computer architecture Whether you are a student studying computer science an aspiring computer engineer or a professional in the field Fundamentals of Modern Computer Architecture provides the knowledge and skills necessary to understand and design efficient computer systems Embark on a learning journey that unveils the intricacies of computer architecture and equips you with the tools to tackle the challenges of the digital age Note This book assumes basic knowledge of digital logic and computer organization It is suitable for undergraduate and graduate students as well as professionals seeking a comprehensive resource on modern computer architecture **Computer Architecture** John L. Hennessy,David A. Patterson,2002-05-29 This best selling title considered for over a decade to be essential reading for every serious student and practitioner of computer design has been updated throughout to address the most important trends facing computer designers today In this edition the authors bring their trademark method of quantitative analysis not only to high performance desktop machine design but also to the design of embedded and server systems They have illustrated their principles with designs from all three of these domains including examples from consumer electronics multimedia and web technologies and high performance computing The book retains its highly rated features Fallacies and Pitfalls which share the hard won lessons of real designers Historical Perspectives which provide a deeper look at computer design history Putting it all Together which present a design example that illustrates the principles of the chapter Worked Examples which

challenge the reader to apply the concepts theories and methods in smaller scale problems and Cross Cutting Issues which show how the ideas covered in one chapter interact with those presented in others In addition a new feature Another View presents brief design examples in one of the three domains other than the one chosen for Putting It All Together The authors present a new organization of the material as well reducing the overlap with their other text Computer Organization and Design A Hardware Software Approach 2 e and offering more in depth treatment of advanced topics in multithreading instruction level parallelism VLIW architectures memory hierarchies storage devices and network technologies Also new to this edition is the adoption of the MIPS 64 as the instruction set architecture In addition to several online appendixes two new appendixes will be printed in the book one contains a complete review of the basic concepts of pipelining the other provides solutions a selection of the exercises Both will be invaluable to the student or professional learning on her own or in the classroom Hennessy and Patterson continue to focus on fundamental techniques for designing real machines and for maximizing their cost performance Presents state of the art design examples including IA 64 architecture and its first implementation the Itanium Pipeline designs for Pentium III and Pentium IV The cluster that runs the Google search engine EMC storage systems and their performance Sony Playstation 2 Infiniband a new storage area and system area network SunFire 6800 multiprocessor server and its processor the UltraSPARC III Trimedia TM32 media processor and the Transmeta Crusoe processor Examines quantitative performance analysis in the commercial server market and the embedded market as well as the traditional desktop market Updates all the examples and figures with the most recent benchmarks such as SPEC 2000 Expands coverage of instruction sets to include descriptions of digital signal processors media processors and multimedia extensions to desktop processors Analyzes capacity cost and performance of disks over two decades Surveys the role of clusters in scientific computing and commercial computing Presents a survey taxonomy and the benchmarks of errors and failures in computer systems Presents detailed descriptions of the design of storage systems and of clusters Surveys memory hierarchies in modern microprocessors and the key parameters of modern disks Presents a glossary of networking terms   *Essentials of Computer Architecture* Douglas Comer,2017-01-06 This easy to read textbook provides an introduction to computer architecture while focusing on the essential aspects of hardware that programmers need to know The topics are explained from a programmer s point of view and the text emphasizes consequences for programmers Divided in five parts the book covers the basics of digital logic gates and data paths as well as the three primary aspects of architecture processors memories and I O systems The book also covers advanced topics of parallelism pipelining power and energy and performance A hands on lab is also included The second edition contains three new chapters as well as changes and updates throughout   Advanced Computer Architecture Kai Hwang,1993 This text presents the latest technologies for parallel processing and high performance computing It deals with advanced computer architecture and parallel processing systems and techniques providing an integrated study of computer hardware and software systems and the material is

suitable for use on courses found in computer science computer engineering or electrical engineering departments

Advanced Computer Architecture, 2E Kai Hwang,2011 **Advanced Computer Architecture** Parthasarthy,

Advanced Computer Architectures Desző Sima,Péter Kacsuk,1997 This timely book provides an unconventional and up to date overview of all the important computer architectures and is one of the first texts to present all the relevant concepts of advanced architecture classes by exploring their design spaces Advanced Computer Architectures will prove an indispensable guide for anyone who needs to be acquainted with the relevant concepts and solutions introduced in recent years to the dramatically changing world of computer architecture For the student of advanced level courses in computer architecture this book will provide a comprehensive and accessible overview of the subject whilst its strong practical orientation will make it an invaluable reference for the practitioner Features Explores design spaces for each architecture class and exposes evolution of concepts and design issues Provides an up to date overview of significant architecture classes including unique in depth coverage of superscalar architectures as well as multithreaded shared and distributed memory MIMDs and associative and neural architectures Identifies which concepts and design choices have been made use of in important processors and illustrates significant trends and surpassed and viable concepts Case studies and tables show microarchitectural details of relevant processors including the PentiumPro PowerPC 604 PowerPC 620 and R10000 allowing comparisons between them 0201422913B04062001 **Advances in Computers** Marvin Zelkowitz,2004-05-26 Advances in Computers covers new developments in computer technology Most chapters present an overview of a current subfield within computer science with many citations and often include new developments in the field by the authors of the individual chapters Topics include hardware software theoretical underpinnings of computing and novel applications of computers This current volume emphasizes architectural issues in the design of new hardware and software system An architectural design evaluation process is described that allows developers to make sure that their source programs adhere to the architectural design of the specifications This greatly aids in the maintenance of the system Telecommunications issues are covered from the impact of new technology to security of wireless systems Quantum computing an exciting development that may greatly increase the speed of present computers is described The book series is a valuable addition to university courses that emphasize the topics under discussion in that particular volume as well as belonging on the bookshelf of industrial practitioners who need to implement many of the technologies that are described In depth surveys and tutorials on new computer technology Well known authors and researchers in the field Extensive bibliographies with most chapters All chapters discuss aspects of architectural design of new hardware and software Quantum computing is an exciting new prospect for future machine design **Advances in Computer Architecture** Glenford J. Myers,1978 A completely updated edition of this overview of modern computer architecture Examines alternatives to classical low level von Neumann computer architecture discussing the problems of classical architecture and new solutions to these problems Illustrates new concepts

through in depth case studies of the Intel APX 432 IBM s SWARD and other machines State of the art concepts covered include tagged storage capability based addressing process management protection domains and error detection

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