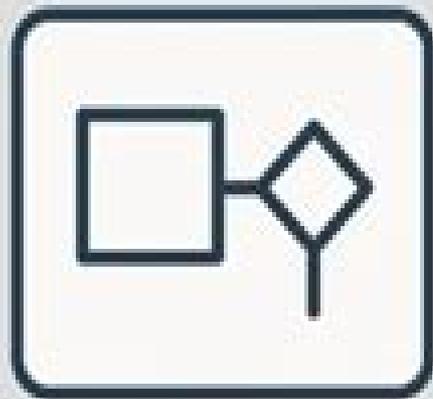


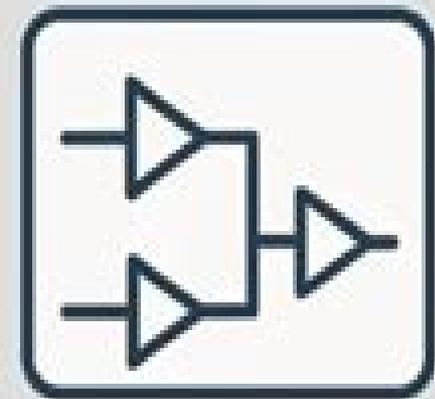
LOGIC SYNTHESIS IN VLSI



**RTL
INPUT**



**SYNTHESIS
TOOL**



**GATE-LEVEL
OUTPUT**

Vlsi Logic Synthesis Design

John Williams



Vlsi Logic Synthesis Design:

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